# NST3906DP6T5G

# **Dual General Purpose** Transistor

The NST3906DP6T5G device is a spin-off of our popular SOT-23/SOT-323/SOT-563 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-963 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

# Features

- h<sub>FE</sub>, 100–300
- Low  $V_{CE(sat)}$ ,  $\leq 0.4 V$
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- This is a Pb–Free Device

# **MAXIMUM RATINGS**

Rating		Symbol	Value	Unit
Collector – Emitter Voltage		V <sub>CEO</sub>	-40	V
Collector – Base Voltage		V <sub>CBO</sub>	-40	V
Emitter – Base Voltage		V <sub>EBO</sub>	-5.0	V
Collector Current – Continuous		Ι <sub>C</sub>	-200	mA
Electrostatic Discharge	HBM MM	ESD Class	2 B	

# THERMAL CHARACTERISTICS

Characteristic (Single Heated)	Symbol	Мах	Unit
Total Device Dissipation T <sub>A</sub> = 25°C Derate above 25°C (Note 1)	PD	240 1.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	520	°C/W
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 2)	P <sub>D</sub>	280 2.2	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\thetaJA}$	446	°C/W
Characteristic (Dual Heated) (Note 3)	Symbol	Мах	Unit
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 1)	PD	350 2.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	357	°C/W
Total Device Dissipation $T_A = 25^{\circ}C$ Derate above 25°C (Note 2)	P <sub>D</sub>	420 3.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\thetaJA}$	297	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	– 55 to +150	°C

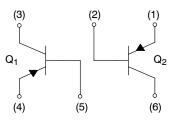
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
1. FR-4 @ 100 mm<sup>2</sup>, 1 oz. copper traces, still air.
2. FR-4 @ 500 mm<sup>2</sup>, 1 oz. copper traces, still air.

3. Dual heated values assume total power is sum of two equally powered channels.



# **ON Semiconductor®**

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NST3906DP6T5G



SOT-963 CASE 527AD

## MARKING DIAGRAM



F = Device Code

M = Date Code

## **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NST3906DP6T5G	SOT–963 (Pb–Free)	8000/Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NST3906DP6T5G

# **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector – Emitter Breakdown Voltage (Note 4) ( $I_C = 1.0 \text{ mAdc}, I_B = 0$ )	V <sub>(BR)CEO</sub>	-40	-	V
Collector – Base Breakdown Voltage ( $I_C = 10 \ \mu Adc$ , $I_E = 0$ )	V <sub>(BR)CBO</sub>	-40	-	V
Emitter – Base Breakdown Voltage ( $I_E = 10 \ \mu Adc, I_C = 0$ )	V <sub>(BR)EBO</sub>	-5.0	-	V
Collector Cutoff Current ( $V_{CE}$ = 30 Vdc, $V_{EB}$ = 3.0 Vdc)	I <sub>CEX</sub>	_	-50	nA

# **ON CHARACTERISTICS** (Note 4)

DC Current Gain	h <sub>FE</sub>			-
(I <sub>C</sub> = -0.1 mA, V <sub>CE</sub> = -1.0 V)		60	-	
(I <sub>C</sub> = -1.0 mA, V <sub>CE</sub> = -1.0 V)		80	-	
(I <sub>C</sub> = -10 mA, V <sub>CE</sub> = -1.0 V)		100	300	
$(I_{C} = -50 \text{ mA}, V_{CE} = -1.0 \text{ V})$		60	-	
(I <sub>C</sub> = -100 mA, V <sub>CE</sub> = -1.0 V)		30	-	
Collector – Emitter Saturation Voltage	V <sub>CE(sat)</sub>			V
$(I_{\rm C} = -10 \text{ mA}, I_{\rm B} = -1.0 \text{ mA})$	()	-	-0.25	
(I <sub>C</sub> = –50 mA, I <sub>B</sub> = –5.0 mA)		-	-0.4	
Base – Emitter Saturation Voltage	V <sub>BE(sat)</sub>			V
$(I_{\rm C} = -10 \text{ mA}, I_{\rm B} = -1.0 \text{ mA})$	· · · · ·	-0.65	-0.85	
(I <sub>C</sub> = –50 mA, I <sub>B</sub> = –5.0 mA)		-	-0.95	

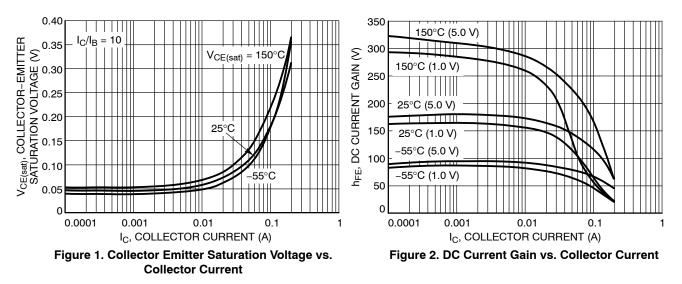
#### SMALL-SIGNAL CHARACTERISTICS

Current – Gain – Bandwidth Product ( $I_C$ = 10 mAdc, $V_{CE}$ = 20 Vdc, f = 100 MHz)	f <sub>T</sub>	250	-	MHz
Output Capacitance (V <sub>CB</sub> = $-5.0$ V, I <sub>E</sub> = 0 mA, f = 1.0 MHz)	C <sub>obo</sub>	-	4.5	pF
Input Capacitance ( $V_{EB} = -0.5 \text{ V}$ , $I_E = 0 \text{ mA}$ , f = 1.0 MHz)	C <sub>ibo</sub>	-	10.0	pF
Noise Figure (V <sub>CE</sub> = –5.0 V, I <sub>C</sub> = –100 $\mu$ A, R <sub>S</sub> = 1.0 k $\Omega$ , f = 1.0 kHz)	NF	-	4.0	dB

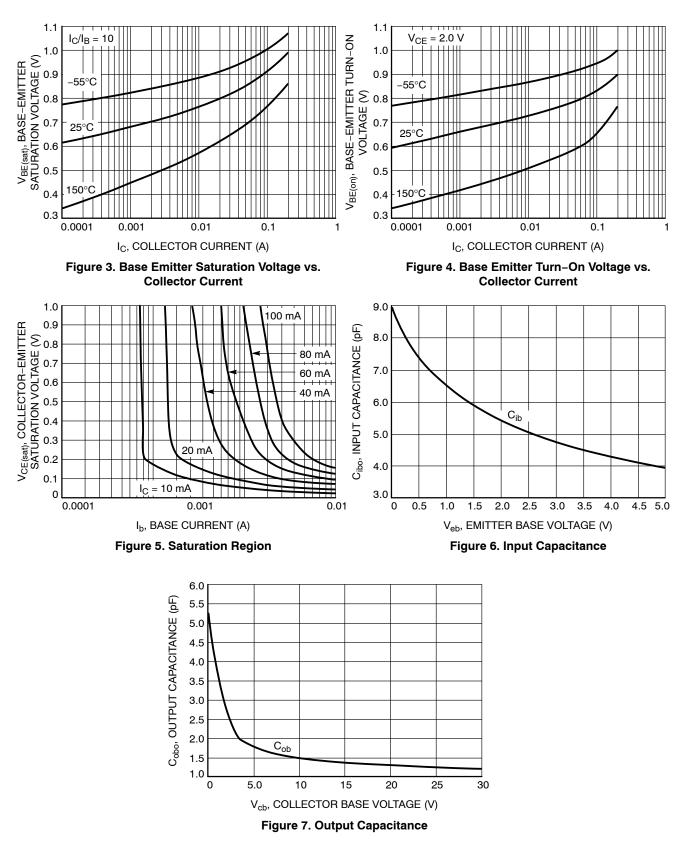
# SWITCHING CHARACTERISTICS

Delay Time	$(V_{CC} = -3.0 \text{ V}, \text{ V}_{BE} = 0.5 \text{ V})$	t <sub>d</sub>	-	35	20
Rise Time	(I <sub>C</sub> = -10 mA, I <sub>B1</sub> = -1.0 mA)	t <sub>r</sub>	-	35	ns
Storage Time	$(V_{CC} = -3.0 \text{ V}, I_C = -10 \text{ mA})$	ts	-	250	20
Fall Time	(I <sub>B1</sub> = I <sub>B2</sub> = -1.0 mA)	t <sub>f</sub>	-	50	ns

4. Pulse Test: Pulse Width  $\leq$  300  $\mu$ s; Duty Cycle  $\leq$  2.0%.



# NST3906DP6T5G



# )nsem<sup>[</sup>

DATE 09 FEB 2010



SCALE 4:1

STYLE 1:

PIN 1. EMITTER 1

STYLE 4: PIN 1. COLLECTOR 2. COLLECTOR 3. BASE 4. EMITTER

STYLE 7: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE

5. ANODE 6. CATHODE

STYLE 10: PIN 1. CATHODE 1 2. N/C

5. N/C

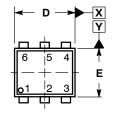
3. CATHODE 2 4. ANODE 2

6. ANODE 1

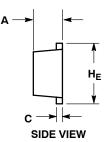
5. COLLECTOR 6. COLLECTOR

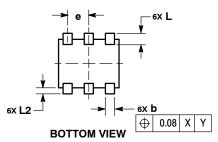
2. BASE 1 3. COLLECTOR 2 4. EMITTER 2

5. BASE 2 6. COLLECTOR 1



**TOP VIEW** 





STYLE 2

PIN 1. EMITTER 1

STYLE 5: PIN 1. CATHODE 2. CATHODE 3. ANODE 4. ANODE

STYLE 8: PIN 1. DRAIN

5 DRAIN

2. DRAIN

6. DRAIN

3. GATE 4. SOURCE

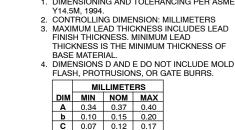
5. CATHODE 6. CATHODE

2. EMITTER2

3. BASE 2 4. COLLECTOR 2

5. BASE 1 6. COLLECTOR 1

SOT-963 CASE 527AD ISSUE E



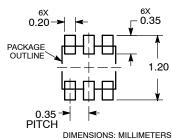
NOTES:

2. 3.

> A
>  0.34
>  0.37
>  0.40
>
>
>  b
>  0.10
>  0.15
>  0.20
>  С 0.07 0.12 0.17 0.951.001.050.750.800.85 D Е 0.35 BSC е ΗE 0.95 1.00 1.05 Т 0.19 REF L2 0.05 0.10 0.15

1. DIMENSIONING AND TOLERANCING PER ASME

#### RECOMMENDED **MOUNTING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



= Month Code М

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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STYLE 3

PIN 1. CATHODE 1 2. CATHODE 1

STYLE 6: PIN 1. CATHODE 2. ANODE 3. CATHODE 4. CATHODE 5. CATHODE 6. CATHODE

STYLE 9: PIN 1. SOURCE 1 2. GATE 1

3. DRAIN 2

5. GATE 2 6. DRAIN 1

4. SOURCE 2

3. ANODE/ANODE 2 4. CATHODE 2

5. CATHODE 2 6. ANODE/ANODE 1

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