

# BSP16T1G

## High Voltage Transistors

### PNP Silicon

#### Features

- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CEO}$	-300	Vdc
Collector-Base Voltage	$V_{CBO}$	-350	Vdc
Emitter-Base Voltage	$V_{EBO}$	-6.0	Vdc
Collector Current	$I_C$	-100	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ (Note 1)	$P_D$	1.5	W
Storage Temperature Range	$P_D$	-65 to +150	$^\circ\text{C}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	83.3	$^\circ\text{C}/\text{W}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

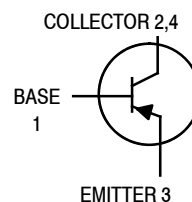
1. Device mounted on a glass epoxy printed circuit board 1.575 in x 1.575 in x 0.059 in; mounting pad for the collector lead min. 0.93 sq. in.



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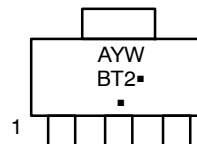
### PNP SILICON HIGH VOLTAGE TRANSISTOR SURFACE MOUNT



#### MARKING DIAGRAM



TO-223  
CASE 318E  
STYLE 1



A = Assembly Location  
Y = Year  
W = Work Week  
BT2 = Device Code  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

#### ORDERING INFORMATION

Device	Package	Shipping†
BSP16T1G	TO-223 (Pb-Free)	1000/Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# BSP16T1G

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector - Emitter Breakdown Voltage ( $I_C = -50 \text{ mAdc}$ , $I_B = 0$ , $L = 25 \text{ mH}$ )	$V_{(BR)CEO}$	-300	-	Vdc
Collector - Base Breakdown Voltage ( $I_C = -100 \text{ }\mu\text{Adc}$ , $I_E = 0$ )	$V_{(BR)CBO}$	-300	-	Vdc
Collector-Emitter Cutoff Current ( $V_{CE} = -250 \text{ Vdc}$ , $I_B = 0$ )	$I_{CES}$	-	-50	$\mu\text{Adc}$
Collector-Base Cutoff Current ( $V_{CB} = -280 \text{ Vdc}$ , $I_E = 0$ )	$I_{CBO}$	-	-1.0	$\mu\text{Adc}$
Emitter-Base Cutoff Current ( $V_{EB} = -6.0 \text{ Vdc}$ , $I_C = 0$ )	$I_{EBO}$	-	-20	$\mu\text{Adc}$
<b>ON CHARACTERISTICS</b>				
DC Current Gain ( $V_{CE} = -10 \text{ Vdc}$ , $I_C = -50 \text{ mAdc}$ )	$h_{FE}$	30	120	-
Collector-Emitter Saturation Voltage ( $I_C = -50 \text{ mAdc}$ , $I_B = -5.0 \text{ mAdc}$ )	$V_{CE(sat)}$	-	-2.0	Vdc
<b>DYNAMIC CHARACTERISTICS</b>				
Current Gain - Bandwidth Product ( $V_{CE} = -10 \text{ Vdc}$ , $I_C = -10 \text{ mAdc}$ , $f = 30 \text{ MHz}$ )	$f_T$	15	-	MHz
Collector-Base Capacitance ( $V_{CB} = -10 \text{ Vdc}$ , $I_E = 0$ , $f = 1.0 \text{ MHz}$ )	$C_{obo}$	-	15	pF

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®



SCALE 1:1

SOT-223 (TO-261)  
CASE 318E-04  
ISSUE R

DATE 02 OCT 2018



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.200MM PER SIDE.
4. DATUMS A AND B ARE DETERMINED AT DATUM H.
5. A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT OF THE PACKAGE BODY.
6. POSITIONAL TOLERANCE APPLIES TO DIMENSIONS b AND b1.

MILLIMETERS			
DIM	MIN.	NOM.	MAX.
A	1.50	1.63	1.75
A1	0.02	0.06	0.10
b	0.60	0.75	0.89
b1	2.90	3.06	3.20
c	0.24	0.29	0.35
D	6.30	6.50	6.70
E	3.30	3.50	3.70
e	2.30 BSC		
L	0.20	---	---
L1	1.50	1.75	2.00
He	6.70	7.00	7.30
θ	0°	---	10°



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DESCRIPTION:	SOT-223 (TO-261)	PAGE 1 OF 2

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**SOT-223 (TO-261)**  
**CASE 318E-04**  
**ISSUE R**

DATE 02 OCT 2018

- |  |   |   |   |   |
|--|---|---|---|---|
| <b>STYLE 1:</b><br>PIN 1. BASE<br>2. COLLECTOR<br>3. EMITTER<br>4. COLLECTOR | <b>STYLE 2:</b><br>PIN 1. ANODE<br>2. CATHODE<br>3. NC<br>4. CATHODE        | <b>STYLE 3:</b><br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE<br>4. DRAIN           | <b>STYLE 4:</b><br>PIN 1. SOURCE<br>2. DRAIN<br>3. GATE<br>4. DRAIN   | <b>STYLE 5:</b><br>PIN 1. DRAIN<br>2. GATE<br>3. SOURCE<br>4. GATE    |
| <b>STYLE 6:</b><br>PIN 1. RETURN<br>2. INPUT<br>3. OUTPUT<br>4. INPUT        | <b>STYLE 7:</b><br>PIN 1. ANODE 1<br>2. CATHODE<br>3. ANODE 2<br>4. CATHODE | <b>STYLE 8:</b><br>CANCELLED  | <b>STYLE 9:</b><br>PIN 1. INPUT<br>2. GROUND<br>3. LOGIC<br>4. GROUND | <b>STYLE 10:</b><br>PIN 1. CATHODE<br>2. ANODE<br>3. GATE<br>4. ANODE |
| <b>STYLE 11:</b><br>PIN 1. MT 1<br>2. MT 2<br>3. GATE<br>4. MT 2             | <b>STYLE 12:</b><br>PIN 1. INPUT<br>2. OUTPUT<br>3. NC<br>4. OUTPUT         | <b>STYLE 13:</b><br>PIN 1. GATE<br>2. COLLECTOR<br>3. EMITTER<br>4. COLLECTOR |   |   |

**GENERIC  
 MARKING DIAGRAM\***



- A = Assembly Location
- Y = Year
- W = Work Week
- XXXXX = Specific Device Code
- = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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