## BC858CDXV6T1, BC858CDXV6T5

## Dual General Purpose Transistor

## **PNP** Dual

This transistor is designed for general purpose amplifier applications. It is housed in the SOT–563 which is designed for low power surface mount applications.

## Features

• These are Pb–Free Devices

## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	-30	V
Collector-Base Voltage	V <sub>CBO</sub>	-30	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5.0	V
Collector Current – Continuous	Ι <sub>C</sub>	-100	mAdc

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS

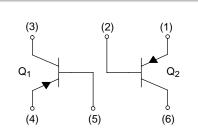
Characteristic (One Junction Heated)	Symbol	Мах	Unit
Total Device Dissipation, (Note 1) $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	P <sub>D</sub>	357 2.9	mW mW/°C
Thermal Resistance Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	350	°C/W
Characteristic (Both Junctions Heated)	Symbol	Мах	Unit
Total Device Dissipation, (Note 1) T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	500 4.0	mW mW/°C
Thermal Resistance Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	250	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

1. FR-4 @ Minimum Pad



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SOT-563 CASE 463A PLASTIC

## MARKING DIAGRAMS



3L = Device Code

M = Date Code

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= Pb-Free Package

(Note: Microdot may be in either location)

## ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>			
BC858CDXV6T1	SOT-563	4000/Tape & Reel			
BC858CDXV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel			
BC858CDXV6T5	SOT-563	8000/Tape & Reel			
BC858CDXV6T5G	SOT-563 (Pb-Free)	8000/Tape & Reel			

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

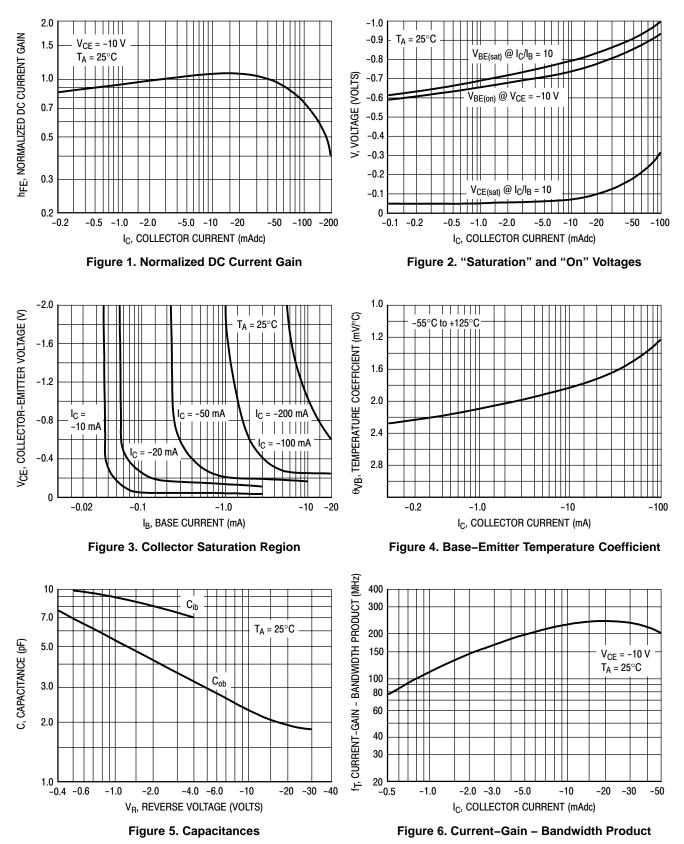
## BC858CDXV6T1, BC858CDXV6T5

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = –10 mA)	V <sub>(BR)CEO</sub>	-30	_	_	V
Collector – Emitter Breakdown Voltage ( $I_C = -10 \ \mu A, \ V_{EB} = 0$ )	V <sub>(BR)CES</sub>	-30	_	_	V
Collector – Base Breakdown Voltage $(I_C = -10 \ \mu A)$	V <sub>(BR)CBO</sub>	-30	-	_	V
Emitter – Base Breakdown Voltage $(I_E = -1.0 \ \mu A)$	V <sub>(BR)EBO</sub>	-5.0	-	_	V
Collector Cutoff Current (V <sub>CB</sub> = $-30$ V) (V <sub>CB</sub> = $-30$ V, T <sub>A</sub> = $150^{\circ}$ C)	I <sub>CBO</sub>	-		-15 -4.0	nA μA
ON CHARACTERISTICS		1			
DC Current Gain $(I_C = -10 \ \mu\text{A}, \ V_{CE} = -5.0 \ \text{V})$ $(I_C = -2.0 \ \text{mA}, \ V_{CE} = -5.0 \ \text{V})$	h <sub>FE</sub>	420	270 520	800	_
Collector – Emitter Saturation Voltage ( $I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$ ) ( $I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$ )	V <sub>CE(sat)</sub>			-0.3 -0.65	V
Base – Emitter Saturation Voltage $(I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA})$ $(I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA})$	V <sub>BE(sat)</sub>		-0.7 -0.9		V
Base – Emitter On Voltage $(I_{C} = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V})$ $(I_{C} = -10 \text{ mA}, V_{CE} = -5.0 \text{ V})$	V <sub>BE(on)</sub>	-0.6 -		-0.75 -0.82	V
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain – Bandwidth Product ( $I_C = -10 \text{ mA}, V_{CE} = -5.0 \text{ Vdc}, f = 100 \text{ MHz}$ )	f <sub>T</sub>	100	-	_	MHz
Output Capacitance ( $V_{CB} = -10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$ )	C <sub>ob</sub>	-	-	4.5	pF
Noise Figure (I <sub>C</sub> = $-0.2$ mA, V <sub>CE</sub> = $-5.0$ Vdc, R <sub>S</sub> = $2.0$ kΩ, f = $1.0$ kHz, BW = $200$ Hz)	NF	-	-	10	dB

## BC858CDXV6T1, BC858CDXV6T5

## **TYPICAL CHARACTERISTICS**



NDTES:

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MILLIMETERS

NDM.

0.55

0.22

0.13

1.60

1.20

0.50 BSC

0.20

1.60

MAX.

0.60

0.27

0.18

1.70

1.30

0.30

1.70

SIDE VIEW

MIN.

0.50

0.17

0.08

1.50

1.10

0.10

1.50



SOT-563, 6 LEAD CASE 463A ISSUE H

DATE 26 JAN 2021

SCALE 4:1

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

А

DIM

Α

b

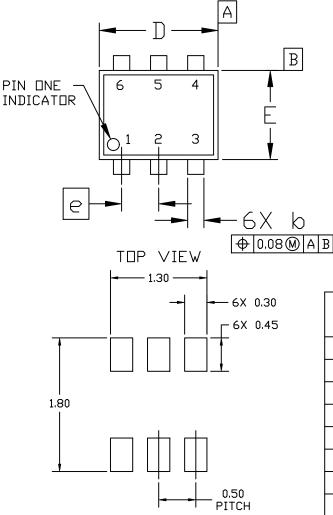
С

D

Ε

e L

Η<sub>E</sub>



PITCH RECOMMENDED MOUNTING FOOTPRINT\* For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	SOT-563, 6 LEAD		PAGE 1 OF 2

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# DUSEM

#### SOT-563, 6 LEAD CASE 463A ISSUE H

DATE 26 JAN 2021

GENERIC			
MARKING DIAGRAM*			

		1
	XX M•	
4	0	
1		

XX = Specific Device Code

M = Month Code

= Pb-Free Package .

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	STYLE 2:	STYLE 3:
PIN 1. EMITTER 1	PIN 1. EMITTER 1	PIN 1. CATHODE 1
2. BASE 1	2. EMITTER 2	2. CATHODE 1
3. COLLECTOR 2	3. BASE 2	3. ANODE/ANODE 2
4. EMITTER 2	4. COLLECTOR 2	4. CATHODE 2
5. BASE 2	5. BASE 1	5. CATHODE 2
6. COLLECTOR 1	6. COLLECTOR 1	6. ANODE/ANODE 1
STYLE 4:	STYLE 5:	STYLE 6:
PIN 1. COLLECTOR	PIN 1. CATHODE	PIN 1. CATHIDE
2. COLLECTOR	2. CATHODE	2. ANIDE
3. BASE	3. ANODE	3. CATHIDE
4. EMITTER	4. ANODE	4. CATHIDE
5. COLLECTOR	5. CATHODE	5. CATHIDE
6. COLLECTOR	6. CATHODE	6. CATHIDE
STYLE 7:	STYLE 8:	STYLE 9:
PIN 1. CATHODE	PIN 1. DRAIN	PIN 1. SDURCE 1
2. ANODE	2. DRAIN	2. GATE 1
3. CATHODE	3. GATE	3. DRAIN 2
4. CATHODE	4. SDURCE	4. SDURCE 2
5. ANODE	5. DRAIN	5. GATE 2
6. CATHODE	6. DRAIN	6. DRAIN 1
STYLE 10: PIN 1. CATHODE 1 2. N/C 3. CATHODE 2 4. ANODE 2 5. N/C 6. ANODE 1	STYLE 11: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	

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