# onsemi

# General Purpose Transistors

# **NPN Silicon**

# **BC846ALT1G Series**

#### Features

- Moisture Sensitivity Level: 1
- ESD Rating Human Body Model: > 4000 V – Machine Model: > 400 V
- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage BC846 BC847, BC850 BC848, BC849	V <sub>CEO</sub>	65 45 30	Vdc
Collector-Base Voltage BC846 BC847, BC850 BC848, BC849	V <sub>CBO</sub>	80 50 30	Vdc
Emitter-Base Voltage BC846 BC847, BC850 BC848, BC849	V <sub>EBO</sub>	6.0 6.0 5.0	Vdc
Collector Current – Continuous	۱ <sub>C</sub>	100	mAdc

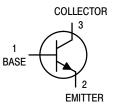
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Total Device Dissipation FR-5 Board, (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	225	mW
Derate above 25°C		1.8	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\thetaJA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2) T <sub>A</sub> = 25°C	P <sub>D</sub>	300	mW
Derate above 25°C		2.4	mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\thetaJA}$	417	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

1. FR–5 = 1.0  $\times$  0.75  $\times$  0.062 in.

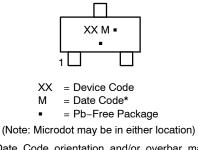
2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in 99.5% alumina.





STYLE 6

#### MARKING DIAGRAM



\*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 12 of this data sheet.

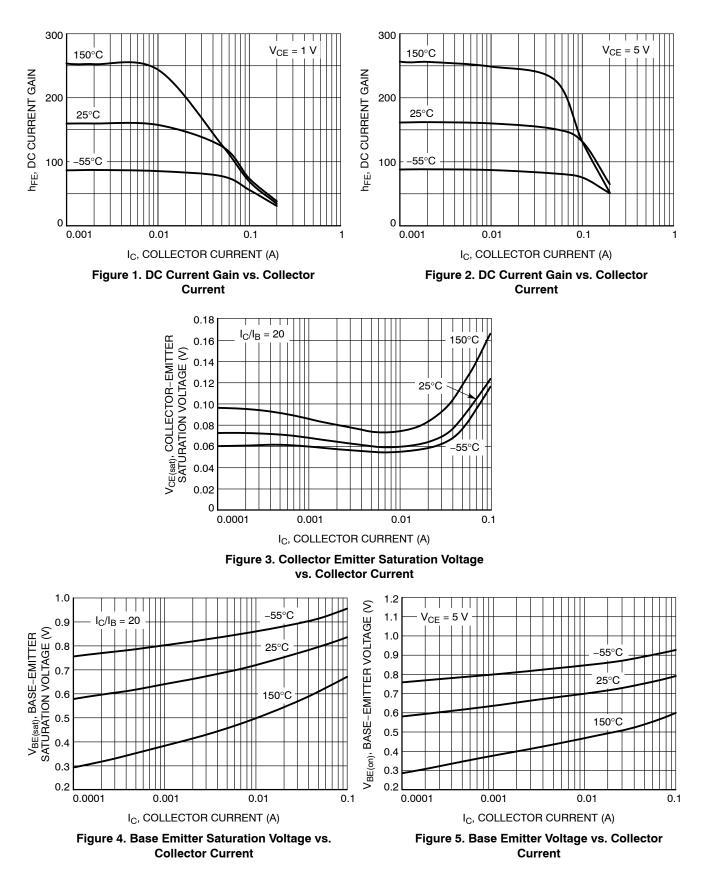
# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characte	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS		•		•		
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = 10 mA)	BC846A, B, C BC847A, B, C, BC850B, C BC848A, B, C, BC849B, C	V <sub>(BR)CEO</sub>	65 45 30		- - -	V
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = 10 $\mu$ A, V <sub>EB</sub> = 0)	BC846A, B, C BC847A, B, C BC850B, C BC848A, B, C, BC849B, C	V <sub>(BR)CES</sub>	80 50 30	- - -	- - -	V
Collector – Base Breakdown Voltage (I <sub>C</sub> = 10 $\mu$ A)	BC846A, B, C BC847A, B, C, BC850B, C BC848A, B, C, BC849B, C	V <sub>(BR)CBO</sub>	80 50 30	- - -	- - -	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = 1.0 $\mu$ A)	BC846A, B, C BC847A, B, C, BC850B, C BC848A, B, C, BC849B, C	V <sub>(BR)EBO</sub>	6.0 6.0 5.0	_ _ _		V
Collector Cutoff Current (V <sub>CB</sub> = 30 V) (V <sub>CB</sub> = 30 V, T <sub>A</sub> = 150°C)	I <sub>CBO</sub>	-		15 5.0	nA μA	
ON CHARACTERISTICS						
DC Current Gain ( $I_C = 10 \mu A, V_{CE} = 5.0 V$ )	BC846A, BC847A, BC848A BC846B, BC847B, BC848B BC846C, BC847C, BC848C	h <sub>FE</sub>		90 150 270	- - -	_
(I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V) BC846C,	BC846A, BC847A, BC848A BC846B, BC847B, BC848B, BC849B, BC850B BC847C, BC848C, BC849C, BC850C		110 200 420	180 290 520	220 450 800	
Collector – Emitter Saturation Voltage ( $I_C$ ( $I_C$	= 10 mA, I <sub>B</sub> = 0.5 mA) = 100 mA, I <sub>B</sub> = 5.0 mA)	V <sub>CE(sat)</sub>	-	-	0.25 0.6	V
Base – Emitter Saturation Voltage ( $I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$ ) ( $I_C = 100 \text{ mA}, I_B = 5.0 \text{ mA}$ )				0.7 0.9		V
Base – Emitter Voltage (I <sub>C</sub> = 2.0 mA, V <sub>CE</sub> = 5.0 V) (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 V)			580 -	660 -	700 770	mV
SMALL-SIGNAL CHARACTERISTICS						
Current-Gain - Bandwidth Product	H <sub>7</sub> )	f <sub>T</sub>	100	-	-	MHz

$(I_c = 10 \text{ mA}, V_{cE} = 5.0 \text{ Vdc}, f = 100 \text{ MHz})$			100	-	-	IVIFIZ	
Output Capacitance (V <sub>CB</sub> = 10 V,	f = 1.0 MHz)	C <sub>obo</sub>	-	-	4.5	pF	
Noise Figure (I <sub>C</sub> = 0.2 mA, V <sub>CE</sub> = 5.0 Vdc, R <sub>S</sub> = 2.0 kΩ, f = 1.0 kHz, BW = 200 Hz)	BC846A,B,C, BC847A,B,C, BC848A,B,C BC849B,C, BC850B,C	NF	-		10 4.0	dB	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### BC846A, BC847A, BC848A, SBC846A



### BC846A, BC847A, BC848A, SBC846A

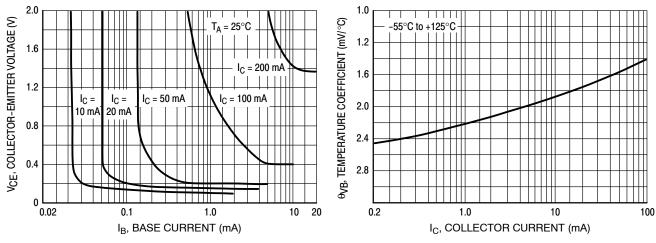


Figure 6. Collector Saturation Region

Figure 7. Base-Emitter Temperature Coefficient

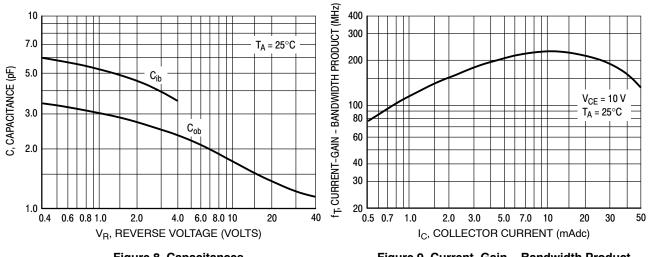
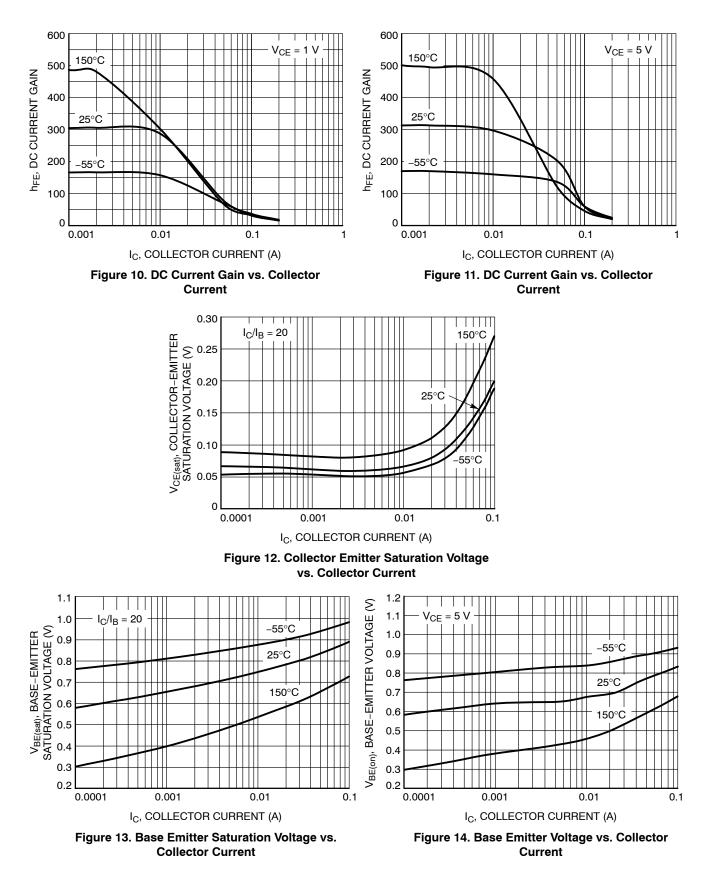


Figure 8. Capacitances

Figure 9. Current–Gain – Bandwidth Product

#### BC846B, SBC846B



#### BC846B, SBC846B

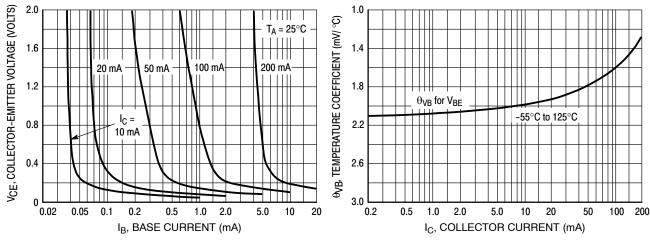


Figure 15. Collector Saturation Region

Figure 16. Base-Emitter Temperature Coefficient

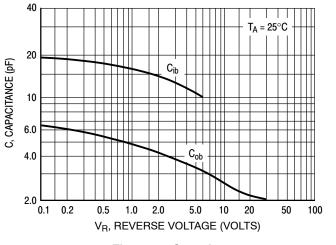


Figure 17. Capacitance

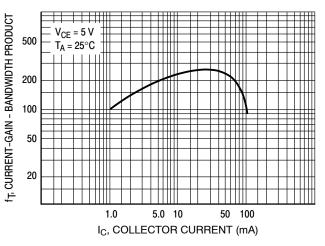
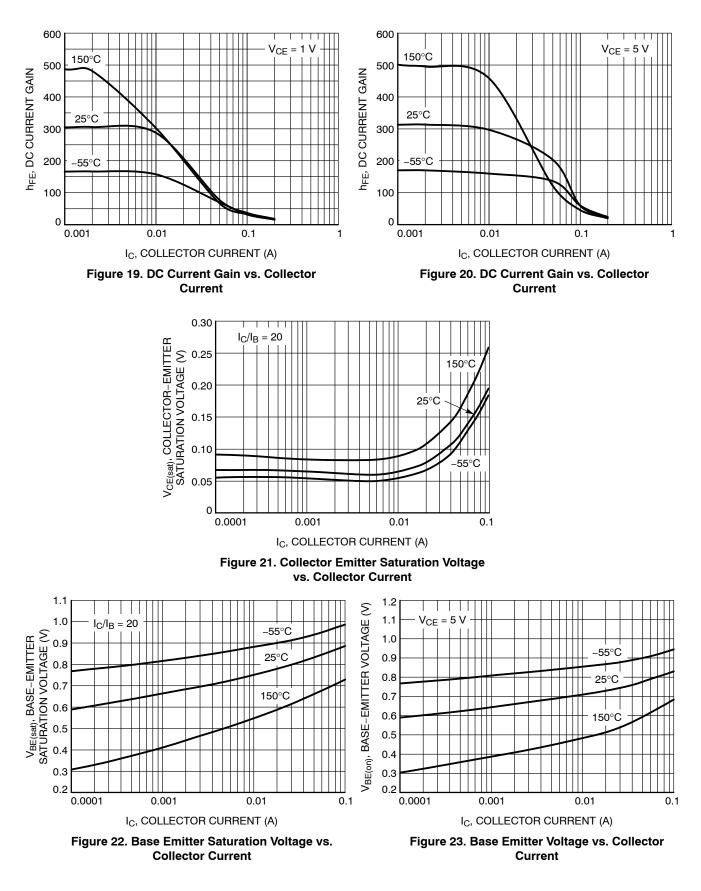
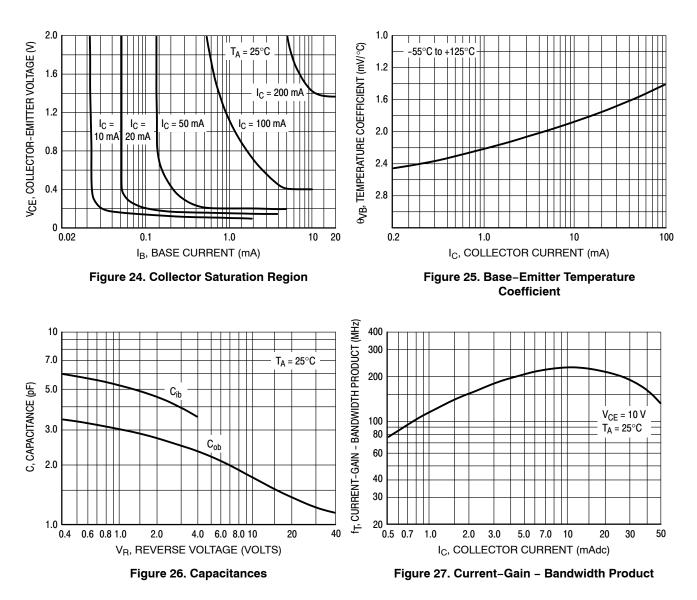


Figure 18. Current–Gain – Bandwidth Product

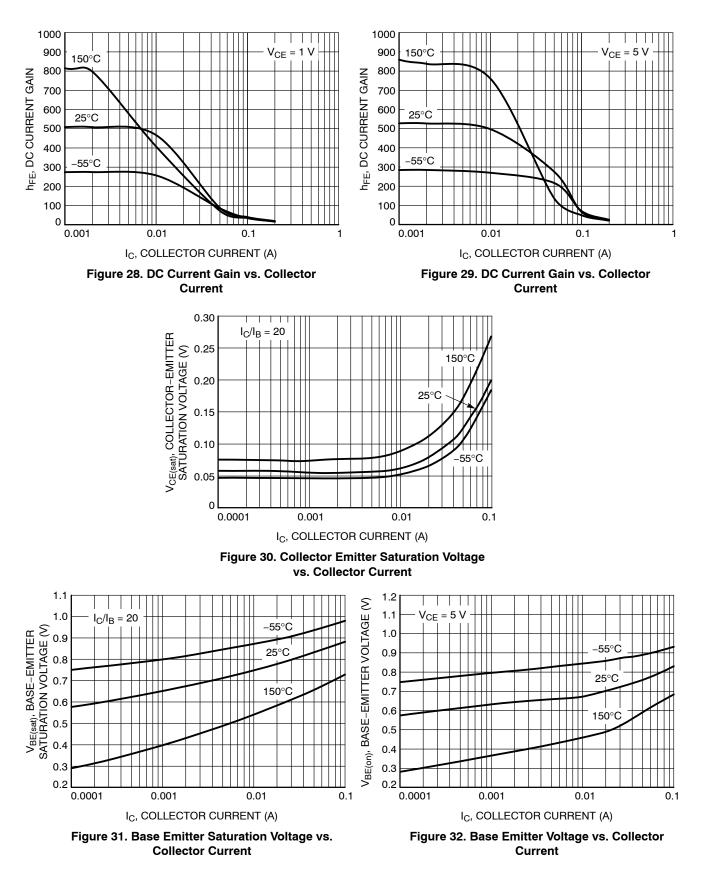
#### BC847B, BC848B, BC849B, BC850B, SBC847B, SBC848B



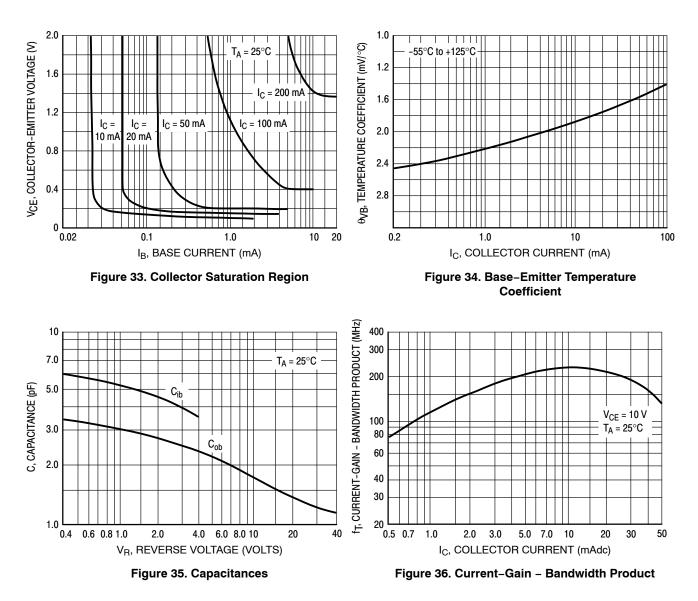
#### BC847B, BC848B, BC849B, BC850B, SBC846B, SBC847B, SBC848B

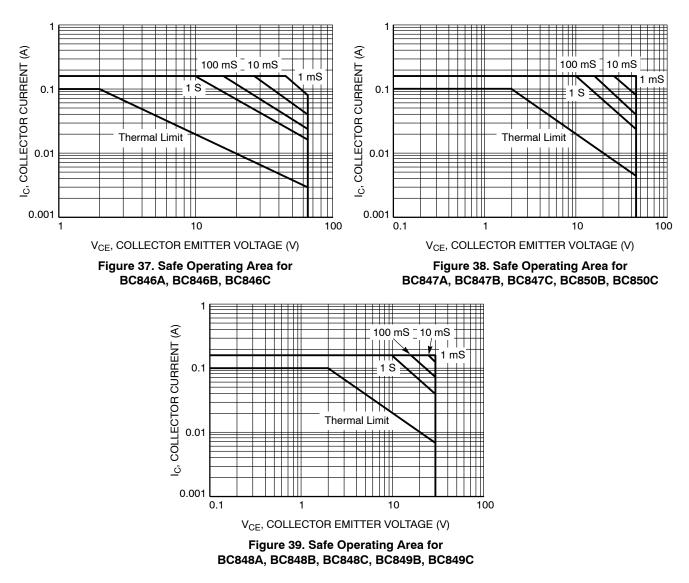


# BC846C, BC847C, BC848C, BC849C, BC850C, SBC847C



### BC846C, BC847C, BC848C, BC849C, BC850C, SBC847C





#### **ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>	
BC846ALT1G			0.000 / T- 0.5 /	
SBC846ALT1G*	1A		3,000 / Tape & Reel	
BC846ALT3G			10,000 / Tape & Reel	
BC846BLT1G				
SBC846BLT1G*		_	3,000 / Tape & Reel	
BC846BLT3G				
SBC846BLT3G*			10,000 / Tape & Reel	
BC846CLT1G	3C		3,000 / Tape & Reel	
BC847ALT1G			3,000 / Tape & Reel	
BC847ALT3G	1E		10,000 / Tape & Reel	
BC847BLT1G		1	0.000 / T- 0.5 1	
SBC847BLT1G*			3,000 / Tape & Reel	
BC847BLT3G	1F			
NSVBC847BLT3G*		-	10,000 / Tape & Reel	
BC847CLT1G				
SBC847CLT1G*	1G		3,000 / Tape & Reel	
BC847CLT3G		SOT-23 (Pb-Free)	10,000 / Tape & Reel	
BC848ALT1G	1J	(	3,000 / Tape & Reel	
BC848BLT1G				
SBC848BLT1G*	1K		3,000 / Tape & Reel	
BC848BLT3G			10,000 / Tape & Reel	
BC848CLT1G				
NSVBC848CLT1G*	1L		3,000 / Tape & Reel	
BC848CLT3G			10,000 / Tape & Reel	
BC849BLT1G				
NSVBC849BLT1G*	2B		3,000 / Tape & Reel	
BC849BLT3G			10,000 / Tape & Reel	
BC849CLT1G	~~~	1	3,000 / Tape & Reel	
BC849CLT3G	2C		10,000 / Tape & Reel	
BC850BLT1G	05	1		
NSVBC850BLT1G*	2F			
BC850CLT1G	~~~	1	3,000 / Tape & Reel	
NSVBC850CLT1G*	2G			

+ For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable.

#### MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

D

3

TOP VIEW

SIDE VIEW

Нe

DETAIL A

-3X b

# onsemi



SCALE 4:1

A\_\_\_\_ ' A1SOT-23 (TO-236) CASE 318 ISSUE AT

0.25

-L1

DETAIL A

END VIEW

DATE 01 MAR 2023

NDTES

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
- 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			INCHES		
DIM	MIN.	NDM.	MAX.	MIN.	NDM.	MAX.
Α	0.89	1.00	1.11	0.035	0.039	0.044
A1	0.01	0.06	0.10	0.000	0.002	0.004
b	0.37	0.44	0.50	0.015	0.017	0.020
с	0.08	0.14	0.20	0.003	0.006	0.008
D	2.80	2.90	3.04	0.110	0.114	0.120
E	1.20	1.30	1.40	0.047	0.051	0.055
e	1.78	1.90	2.04	0.070	0.075	0.080
L	0.30	0.43	0.55	0.012	0.017	0.022
L1	0.35	0.54	0.69	0.014	0.021	0.027
HE	2.10	2.40	2.64	0.083	0.094	0.104
Т	0*		10*	0*		10*



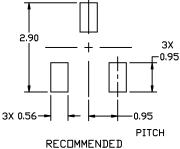


XXX = Specific Device Code

M = Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



MOUNTING FOOTPRINT

\* For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

#### **STYLES ON PAGE 2**

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# MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

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#### SOT-23 (TO-236) CASE 318 ISSUE AT

#### DATE 01 MAR 2023

STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: PIN 1. EMITTER 2. BASE 3. COLLECTOR	STYLE 8: PIN 1. ANODE 2. NO CONNECTION 3. CATHODE		
STYLE 9:	STYLE 10:	STYLE 11:	STYLE 12:	STYLE 13:	STYLE 14:
PIN 1. ANODE	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. SOURCE	PIN 1. CATHODE
2. ANODE	2. SOURCE	2. CATHODE	2. CATHODE	2. DRAIN	2. GATE
3. CATHODE	3. GATE	3. CATHODE-ANODE	3. ANODE	3. GATE	3. ANODE
STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:	STYLE 19:	STYLE 20:
PIN 1. GATE	PIN 1. ANODE	PIN 1. NO CONNECTION	PIN 1. NO CONNECTION	PIN 1. CATHODE	PIN 1. CATHODE
2. CATHODE	2. CATHODE	2. ANODE	2. CATHODE	2. ANODE	2. ANODE
3. ANODE	3. CATHODE	3. CATHODE	3. ANODE	3. CATHODE-ANODE	3. GATE
STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:	STYLE 25:	STYLE 26:
PIN 1. GATE	PIN 1. RETURN	PIN 1. ANODE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE
2. SOURCE	2. OUTPUT	2. ANODE	2. DRAIN	2. CATHODE	2. ANODE
3. DRAIN	3. INPUT	3. CATHODE	3. SOURCE	3. GATE	3. NO CONNECTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE				

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