

## 1ch VIDEO DRIVER WITH SHORT-to-BATTERY PROTECTION

#### **■FEATURES**

Operating Voltage
 Operating Temperature
 Short-to-Battery Protection Circuit of up to 18V

Output Capacitor is unnecessary

•6dB Amp.,  $75\Omega$  Driver

•LPF Characteristics OdB at 6.75MHz

-40dB at 27MHz

CMOS Technology

◆Package Outline DFN8-U1 (ESON8-U1 2mm\*2mm)

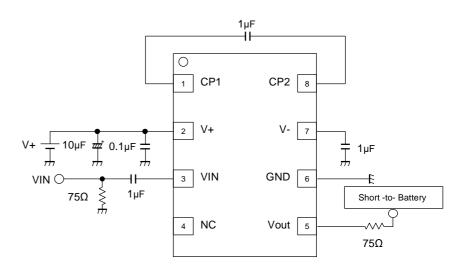
#### **■GENERAL DESCRIPTION**

NJU71091 is 1ch video driver that built in short-to -battery protection circuit of up to 18V. Output capacitor is unnecessary because it built in charge-pump circuit. Therefore, NJU71091 can protect from more than voltage of IC's operating voltage, and is suitable to CAR CAMERA, CAR AV system and so on.

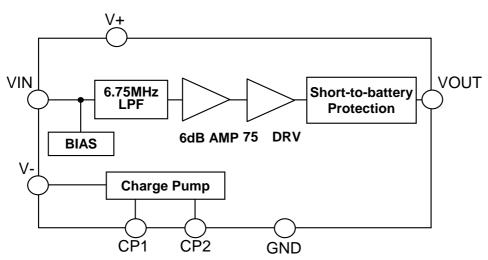
#### **■APPLICATION**

- Car Camera
- Car Navigation

# ■APPLICATION CIRCUIT (At short-to-battery measurement)



#### **■EQUIVALENT CIRCUIT·BLOCK DIAGRAM**



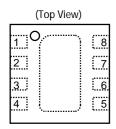
New Japan Radio Co., Ltd.

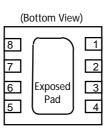


## ■Built in short -to- battery protection circuit video driver

Output type	Part No.
Differential	NJU71094-T1

## **■PIN CONFIGURATION**



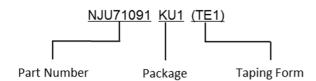


PIN NO.	SYMBOL DESCRIPTION		
1	CP1	Flying Capacitor Terminal	
2	V+	Power Supply Terminal	
3	VIN	Video Signal Input Terminal	
4	N.C.	-	
5	VOUT	Video Signal Output Terminal	
6	GND	GND Terminal	
7	V-	Flying Capacitor Terminal	
8	CP2	Flying Capacitor Terminal	

Exposed Pad:

Connect the Exposed Pad on land of float, Or connect to be the same potential as the IC of the V-terminal.

## **■MARK INFORMATION**



#### **■ORDERING INFORMATION**

PART NUMBER	PACKAGE OUTLINE	RoHS	HALOGEN- FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ(pcs)
NJU71091KU1-T1	ESON8-U1	Yes	Yes	Sn-2Bi	71091T	5.3	3,000



#### **■ABSOLUTE MAXIMUM RATINGS**

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	3.5	V
Power Dissipation (Ta=25°C) <sup>(4)</sup>	$P_{D}$	1500 (1)	mW
Operating Temperature Range	T <sub>opr</sub>	-40 to 125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to 150	°C

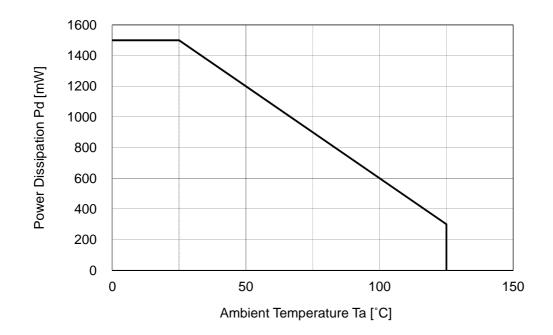
<sup>(1)</sup> Mounted on glass epoxy board. (101.5x114.5x1.6mm: based on EIA/JEDEC standard, 4Layers FR-4, with Exposed Pad) (For 4Layers: Applying 99.5x99.5mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

## **■RECOMMENDED OPERATING CONDITIONS**

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	2.65 to 3.45	V
VIN Input Voltage 1	VIN1	-1.0 to 1.0 (2)	V
VIN Input Voltage 2	VIN2	-0.9 to 0.9 (3)	V

<sup>(2)</sup> V+=2.65 to 3.15V

#### **■POWER DISSIPATION vs. AMBIENT TEMPERATURE**



<sup>(3) 3.15</sup>V<V+≤3.45V



## ■ELECTRICAL CHARACTERISTICS (Ta=25°C, V<sup>+</sup>=3.0V, RL=150Ω, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT	
DC Characteristics							
		No input signal	-	14	27		
Supply Current	Icc	No input signal,	_		27	mA	
		Ta=-40°C to 125°C	-		21		
Video Amplifier Characteris	stics						
Maximum Output		Vin=100kHz, THD=1%,	3.6	-			
Voltage Swing	Vom	Vin=100kHz, THD=1%,	3.6		_	Vp-p	
Vollage Owling		Ta=-40°C to 125°C	3.0		_		
		Vin=100kHz, 1.0Vp-p	5.6	6.0	6.4		
Voltage Gain	Gv	Sine wave	3.0	0.0	0.4	dB	
Vollage Gail I	Gv	Vin=100kHz, 1.0Vp-p	5.6	_	6.4	gB	
		Sine wave, Ta=-40°C to 125°C	5.0	_			
		Vin=6.75MHz/1MHz, 1.0Vpp	-1.0	0	1.0		
	Gf6.75M	Vin=6.75MHz/1MHz, 1.0Vpp	-1.0	_	1.0	dB	
LPF Characteristics		Ta=-40°C to 125°C		_	1.0		
Li i Orialacteristics	Gf27M	Vin=27MHz/100kHz, 1.0Vpp	-	-40	-24		
		Vin=27MHz/100kHz, 1.0Vpp		-24			
		Ta=-40°C to 125°C	_	_	-24		
Differential Gain	DG	Vin=1.0Vp-p,	n=1.0Vp-p,	0.9	_	%	
Diliereridai Gairi	50	10step Video signal	_	0.9		70	
Differential Phase	DP	Vin=1.0Vp-p,	_	1.0	_	deg	
Dillerential Friase	Di	10step Video signal	_	-   1.0   -		ueg	
OALD	ON	RL=75Ω, 1.0Vpp,		70		dB	
S/N Ratio	SN	100% White video signal input, BW=100kHz to 6MHz	-		-		
O Solta Nata La al	NI. I	RL=75 $\Omega$ ,		4.0	7.0		
Switching Noise Level	Nswpl	10% White video signal input	-	4.0	7.0	mVp-p	
Shot-to- Battery Protection							
Protected Maximum Input Voltage (4)	Vstbm	Connected 75 $\Omega$ to Vout(pin5)	-	-	18	V	
		Connected 75Ω to Vout(pin5)	_	_	18		
		Ta=-40°C to 125°C					
Detect Protected Input Voltage (5)	Vth	Connected 75Ω to Vout(pin5)	-	-	6.0	V	
		Connected 75Ω to Vout(pin5) Ta=-40°C to 125°C	-	-	6.0		
		Vout=18V,					
Input Current	Istb	Connected 75 $\Omega$ to Vout(pin5)	-	2.0	-	mA	

<sup>(4)</sup> Maximum input voltage of destination of output resistance  $75\Omega$ 

<sup>(5)</sup> It is become protect mode at more than this voltage against input voltage of destination of output resistance 75ohm.

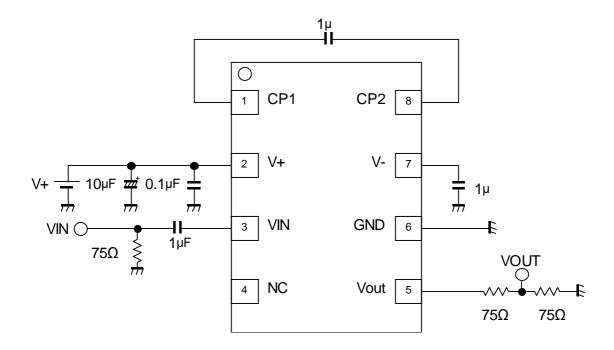
<sup>(6)</sup> If supply voltage is OFF, you must not input the short-to-battery voltage. IC is broken in the worst case.

<sup>(7)</sup> Guaranteed range of Short to Battery voltage is 6V to 18V.



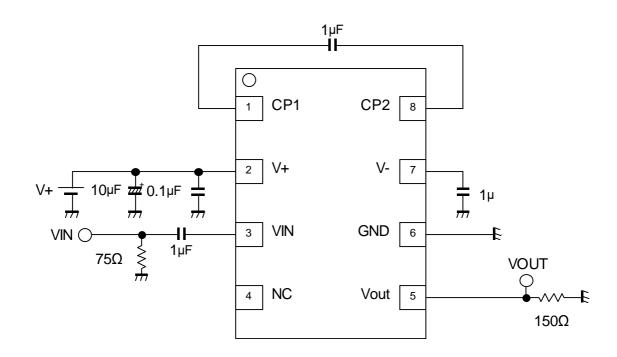
## **■TEST CIRCUIT 1**

(Supply current, Differential gain, Differential phase, Switching noise level, Detect protected Input voltage)



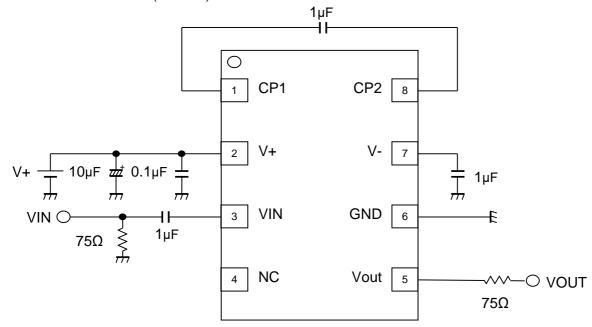
## **■TEST CIRCUIT 2**

(Maximum output level, Voltage gain, Frequency characteristics)





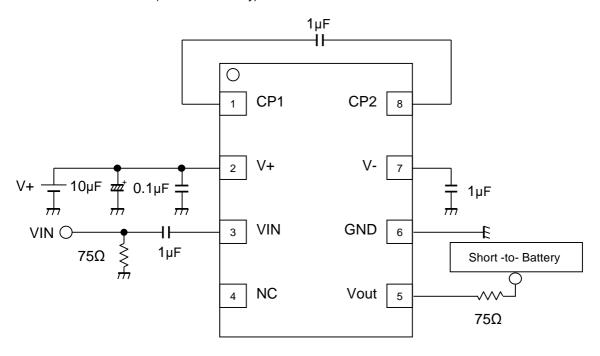
## ■APPLICATION CIRCUIT 1(Standard)



## Exposed Pad:

Connect the Exposed Pad on land of float, or connect to be the same potential as the IC of the V- terminal.

## ■APPLICATION CIRCUIT 2 (At short-to-battery)



## Exposed Pad:

Connect the Exposed Pad on land of float, or connect to be the same potential as the IC of the V- terminal.



#### ■TERMINAL FUNCTION

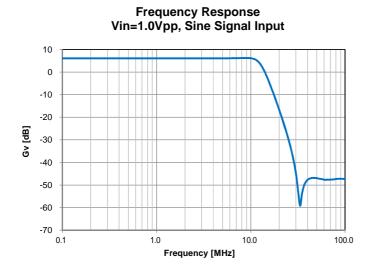
■TERMINAL PINNo.	PINNAME	FUNCTION	EQUIVALENTCIRCUIT	DC VOLTAGE
1	CP1	Flying Capacitor Terminal	V+ ————————————————————————————————————	-
2	V+	V+ Power Supply	-	-
3	VIN	Video Signal Input Terminal	200 200 \$ 150k	OV
4	N.C.	-	-	-
5	VOUT	Video Signal Output Terminal	10k V-	OV

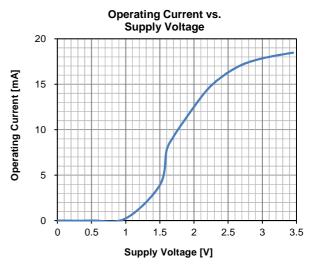


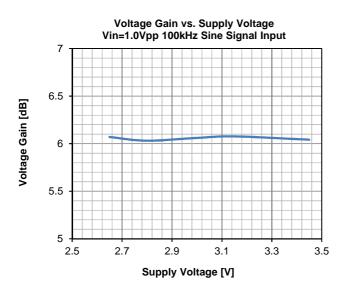
## **■TERMINAL FUNCTION**

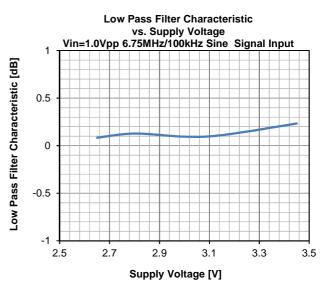
PINNo.	PINNAME	FUNCTION	EQUIVALENTCIRCUIT	DC VOLTAGE
6	GND	Ground	-	-
7	V-	Flying Capacitor Terminal	-	-
8	CP2	Flying Capacitor Terminal	GND	-

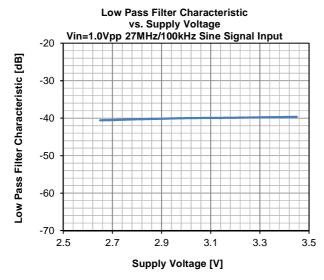




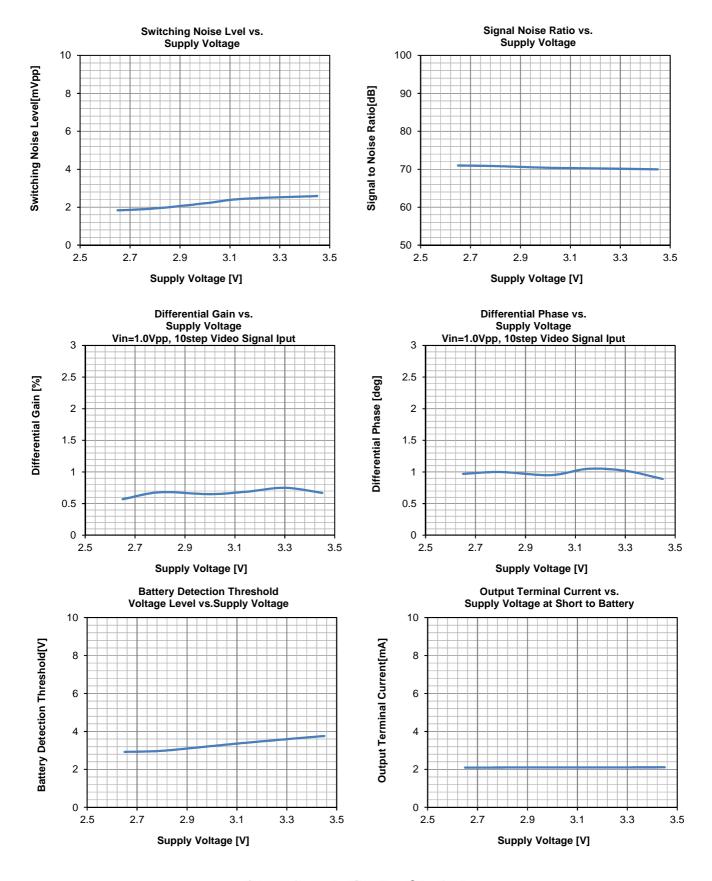




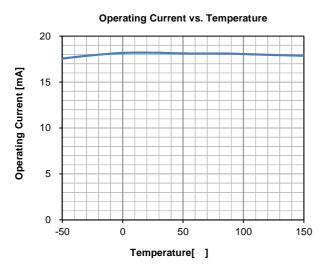


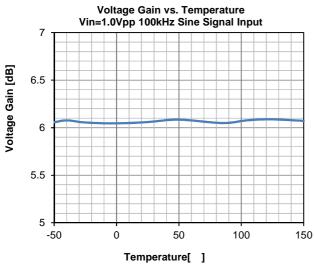


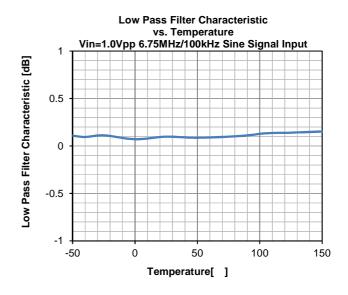


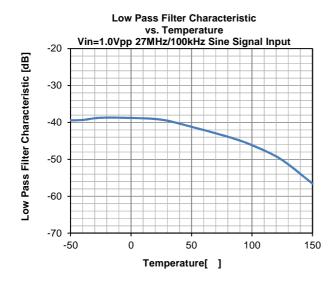




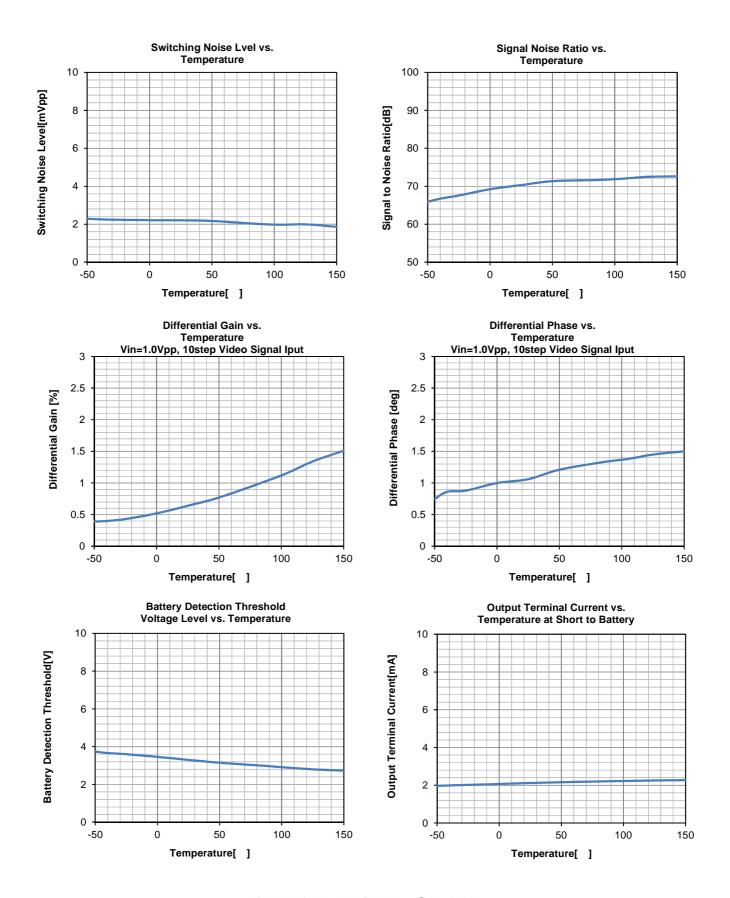








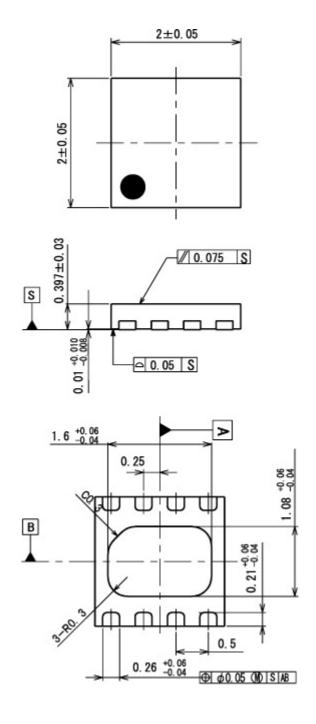






## **■PACKAGE OUTLINE**

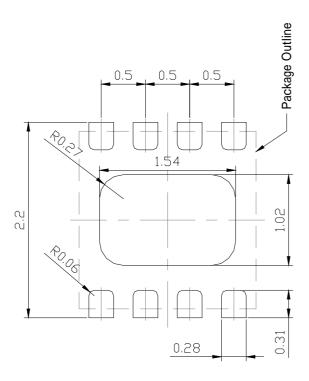
## **DFN8-U1 (ESON8-U1)**



UNIT: mm



## **■SOLDER FOOT PRINT**



Unit: mm

Note: These solder foot print dimensions are just examples.

When designing PCB, please estimate the pattern carefully.



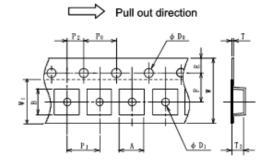
#### **■PACKING SPECIFICATION**

## **General Description**

NJRC delivers ICs in 4 methods, plastic tube container, two kinds of Taping, tray and vinyl bag packing. Except adhesive tape treated anti electrostatic and contain carbon are using as the ESD ( Electrostatic Discharge Damage ) protection.

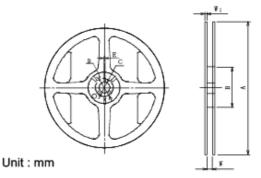
## DFN(ESON) Emboss Taping (TE3)

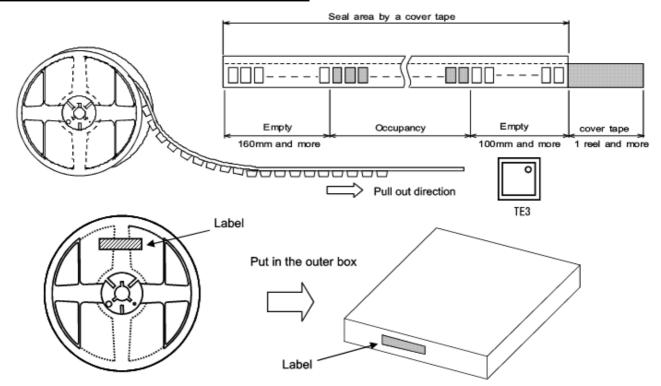
Symbol	DFN8-U1(ESON8-U1)	Remark
Α	2.25±0.05	Bottom size
В	2.25±0.05	Bottom size
D <sub>0</sub>	1.5+0.1/-0	
D1	0.5±0.1	
E	1.75±0.1	
F	3.5 ±0.05	
P <sub>0</sub>	4.0 ±0.1	
P <sub>1</sub>	4.0 ±0.1	
P <sub>2</sub>	2.0 ±0.05	
Т	0.25±0.05	
T <sub>2</sub>	0.75	
W	8.0 ±0.2	
W <sub>1</sub>	5.5	Thickness 0.1MAX



Unit: mm

Symbol	DFN8-U1(ESON8-U1)
A	φ180 +0/-1.5
В	φ 60 +1/-0
С	φ13.0±0.2
D	φ21.0±0.8
E	2.0±0.5
w	9.0 +0.3/-0
W <sub>1</sub>	1.2
Contents	3,000pcs

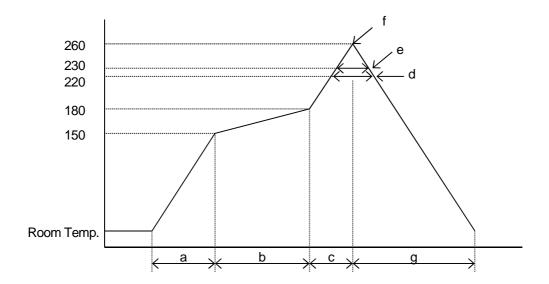






## **■RECOMMENDED MOUNTING METHOD**

\* Recommended reflow soldering procedure



a:Temperature ramping rate
b:Pre-heating temperature
time
: 150 to 180
: 60 to 120s
c:Temperature ramp rate
d:220 or higher time
e:230 or higher time
f:Peak temperature
: 1 to 4 /s
: 5horter than 60s
: Shorter than 40s
: Lower than 260

f:Peak temperature : Lower than g:Temperature ramping rate : 1 to 6 /s

The temperature indicates at the surface of mold package.



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