

<Hybrid-SiC Modules>

CMH150DY-24NFH

HIGH POWER SWITCHING USE **INSULATED TYPE**



dual switch (Half-Bridge)

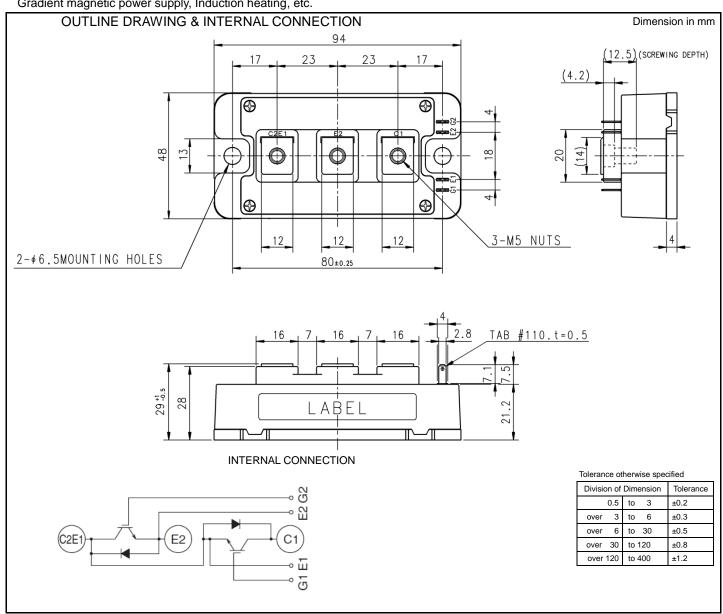
Collector current I_C 150A Collector-emitter voltage V_{CES} 1 2 0 0 V Maximum junction temperature T_{jmax}

- •Silicon IGBT + Silicon Carbide Schottky Barrier Diode
- •Flat base Type
- •Copper base plate
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

High frequency switching use(30kHz to 60kHz)

Gradient magnetic power supply, Induction heating, etc.



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MAXIMUM RATINGS (T_j =25 °C, unless otherwise specified, per 1/2 module)

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V _{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Callactor comment	DC, T _C =25 °C (Note2, 4)	150	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	300	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	960	W
I _E (Note1)	Fig. itter account	DC, T _C =25 °C (Note2, 4)	150	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	300	Α
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
Tj	Junction temperature	-	-40 ~ +150	°C
T _{stq}	Storage temperature	-	-40 ~ +125	1

ELECTRICAL CHARACTERISTICS (T_j=25 °C, unless otherwise specified, per 1/2 module)

Symbol	Item Conditions		Limits			Unit	
Symbol	item	Conditions		Min.	Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	6.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =15 mA, V _{CE} =10 V		4.5	6.0	7.5	V
V	Collector-emitter saturation voltage	I _C =150 A, V _{GE} =15 V (Note5)	T _j =25 °C	-	5.0	6.5	V
V _{CEsat}	Collector-entitler Saturation voltage	Refer to the figure of test circuit	T _j =125 °C	-	5.0	-	V
Cies	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	24	nF
Coes	Output capacitance			-	-	2.0	
Cres	Reverse transfer capacitance	7			-	0.45	
Q_G	Gate charge	V _{CC} =600 V, I _C =150 A, V _{GE} =15 V		-	680	-	nC
t _{d(on)}	Turn-on delay time	V_{CC} =600 V, I_{C} =150 A, V_{GE} =±15 V, R_{G} =2.1 Ω, Inductive load		-	-	150	
tr	Rise time			-	-	80	no
t _{d(off)}	Turn-off delay time			-	-	400	ns -
t _f	Fall time			-	-	150	
V=a (Note1)	Emitter collector voltage	I _E =150 A, G-E short-circuited (Note5)	T _j =25 °C	-	2.2	2.7	V
V _{EC} (Note1)	Emitter-collector voltage	Refer to the figure of test circuit	T _j =125 °C	-	2.9	-	V
Q _C (Note1)	Total capacitive charge	V_{CC} =600 V, I_{E} =150 A, V_{GE} =±15 V, R_{G} =2.1 Ω , Inductive load		-	1.5	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C /I _E =150 A,		-	2.5	-	1
E _{off}	Turn-off switching energy per pulse	V_{GE} =±15 V, R_{G} =2.1 Ω ,	-	-	6.0	-	mJ
E _{rec} (Note1)	Reverse energy per pulse	T _j =125 °C, Inductive load		-	0.5	-	mJ
r _g	Internal gate resistance	Per switch		-	0	-	Ω

THERMAL RESISTANCE CHARACTERISTICS (per 1/2 module)

Symbol	Item	Conditions	Limits			Unit
		Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case (Note4)	-	-	0.13	K/W
$R_{th(j-c)D}$		Junction to case (Note4)	-	-	0.49	r/vv
R _{th(c-s)}	Contact thermal resistance	Case to heat sink, Thermal grease applied (Note4, 6)	-	0.07	-	K/W

Caution; No short-circuit capability is designed.

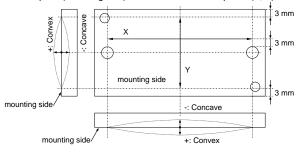
HIGH POWER SWITCHING USE

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MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions Limits Min. Typ.		Conditions		Linit	
				Min.	Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N⋅m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N⋅m
_	Construction of the constr	Terminal to terminal		17.0	-	-	mm
ds	Creepage distance	Terminal to base plate		28.5	-	-	
-1	Clearance	Terminal to terminal		11.0	-	-	mm
da	Clearance	Terminal to base plate		25.6	-	-	
m	mass	-		-	310	-	g
ec	Flatness of base plate	On the centerline X (Note7)		-100	-	100	
	Flatness of base plate	On the centerline Y (Note7)		-100	-	100	μm

- *: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.
- Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (DIODE).
 - 2. Junction temperature (T_j) should not increase beyond $T_{j\,m\,a\,x}$ rating.
 - 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed $T_{j\,m\,a\,x}$ rating.
 - 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
 - 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.
 - 6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
 - 7. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

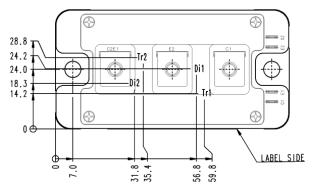


RECOMMENDED OPERATING CONDITIONS

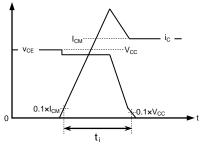
Symbol	ltern Corr	Conditions	Limits			l lait
	Item	Conditions	Min.	Тур.	Max.	Unit
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	800	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	2.1	-	21	Ω

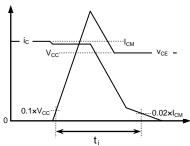
CHIP LOCATION (Top view)

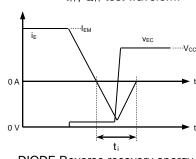
Dimension in mm, tolerance: ±1 mm



Tr1/Tr2: IGBT, Di1/Di2: DIODE





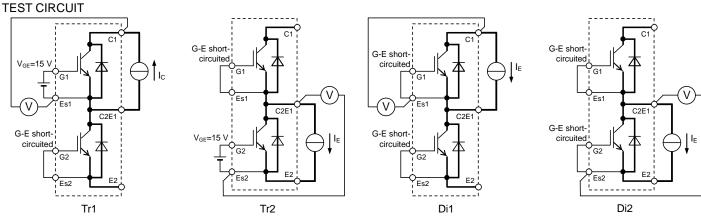


IGBT Turn-on switching energy

IGBT Turn-off switching energy

DIODE Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)



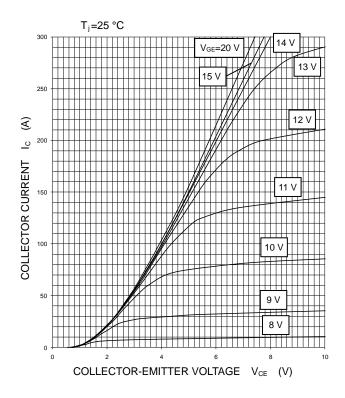
V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

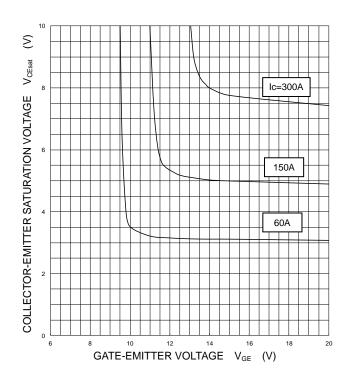
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

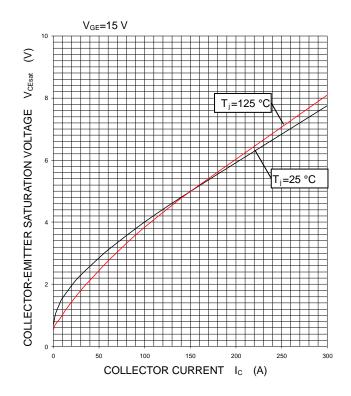
OUTPUT CHARACTERISTICS (TYPICAL)



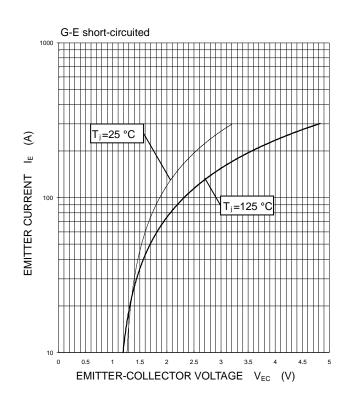
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

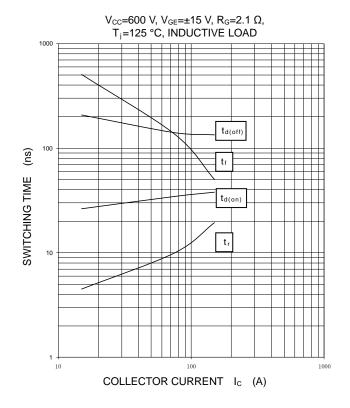


HIGH POWER SWITCHING USE

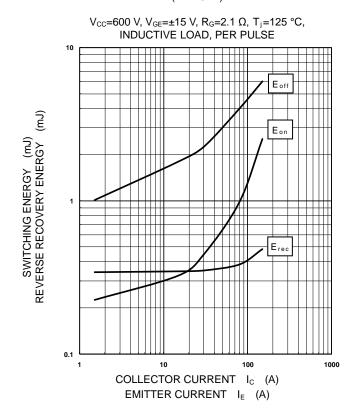
INSULATED TYPE

PERFORMANCE CURVES

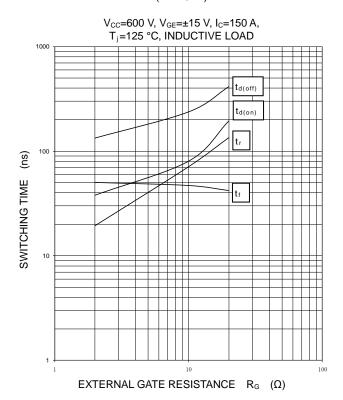
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



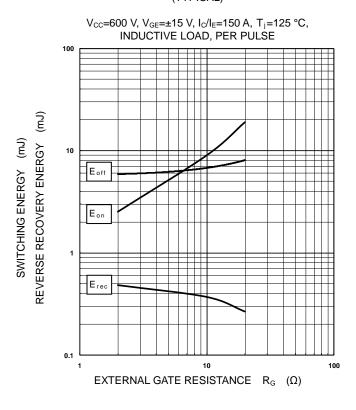
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



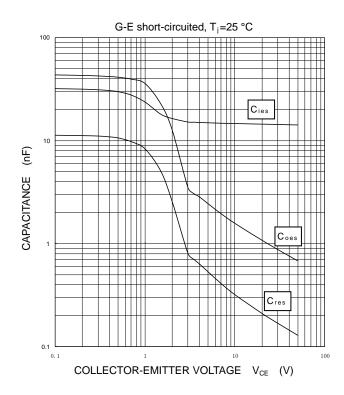
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



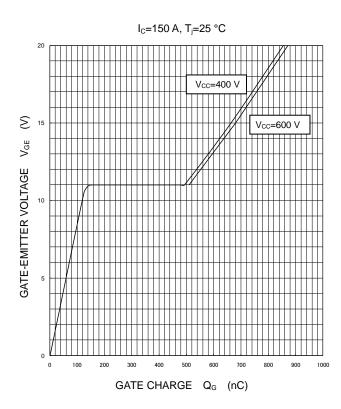
HIGH POWER SWITCHING USE **INSULATED TYPE**

PERFORMANCE CURVES

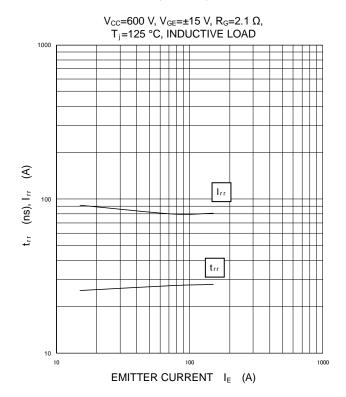
CAPACITANCE **CHARACTERISTICS** (TYPICAL)



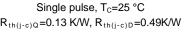
GATE CHARGE CHARACTERISTICS (TYPICAL)

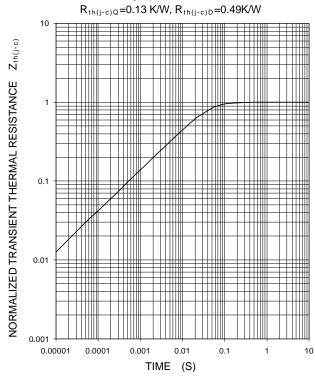


FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTIC S (MAXIMUM)





Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

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