

<Hybrid-SiC Modules>

CMH100DY-24NFH

HIGH POWER SWITCHING USE **INSULATED TYPE**



dual switch (Half-Bridge)

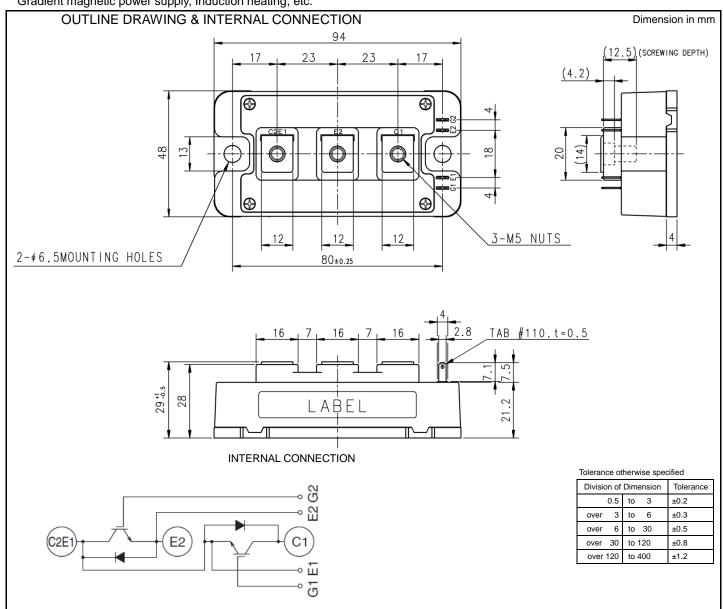
Collector current I_C 100A Collector-emitter voltage V_{CES} 1 2 0 0 V Maximum junction temperature T_{jmax}

- •Silicon IGBT + Silicon Carbide Schottky Barrier Diode
- •Flat base Type
- •Copper base plate
- •RoHS Directive compliant
- •Recognized under UL1557, File E323585

APPLICATION

High frequency switching use(30kHz to 60kHz)

Gradient magnetic power supply, Induction heating, etc.



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MAXIMUM RATINGS (T_j=25 °C, unless otherwise specified, per 1/2 module)

Symbol	Item	Conditions	Rating	Unit	
V _{CES}	Collector-emitter voltage	G-E short-circuited	1200	V	
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current DC, T _C =25 °C (Note2, 4)		100	^	
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	200	A	
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	730	W	
I _E (Note1)	Fitter	DC, T _C =25 °C (Note2, 4)	100	^	
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	200	A	
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V	
T _j	Junction temperature	-	-40 ~ +150	- °C	
T _{stg} Storage temperature		-	-40 ~ +125		

ELECTRICAL CHARACTERISTICS (T_j =25 °C, unless otherwise specified, per 1/2 module)

Cumbal	Item	Conditions	Conditions Limits Min. Typ. M			Unit	
Symbol	Item	Conditions			Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	6.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =10 mA, V _{CE} =10 V		4.5	6.0	7.5	V
V	V _{CEsat} Collector-emitter saturation voltage	I _C =100 A, V _{GE} =15 V (Note5)	T _j =25 °C	-	5.0	6.5	V
V CEsat		Refer to the figure of test circuit	T _j =125 °C	-	5.0	-	V
Cies	Input capacitance		V _{CE} =10 V, G-E short-circuited		-	16	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited			-	1.3	nF
Cres	Reverse transfer capacitance	1		-	-	0.3	
Q _G	Gate charge	V _{CC} =600 V, I _C =100 A, V _{GE} =15 V		-	450	-	nC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =100 A, V _{GE} =±15 V, R _G =3.1 Ω, Inductive load		-	-	100	ns
tr	Rise time			-	-	50	
t _{d(off)}	Turn-off delay time			-	-	250	
t _f	Fall time			-	-	150	
V=o (Note1)	Funition collector voltage	I _E =100 A, G-E short-circuited (Note5)	T _j =25 °C	-	1.7	2.2	
V _{EC} (Note1)	Emitter-collector voltage	Refer to the figure of test circuit	T _j =125 °C	-	2.2	-	_ V
Q _C (Note1)	Total capacitive charge	V_{CC} =600 V, I_{E} =100 A, V_{GE} =±15 V, R_{G} =3.1 Ω , Inductive load		-	1.0	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =600 V, I _C /I _E =100 A,		-	1.7	-	1
E _{off}	Turn-off switching energy per pulse	$V_{GE}=\pm 15 \text{ V}, R_{G}=3.1 \Omega,$		-	4.5	-	mJ
E _{rec} (Note1)	Reverse energy per pulse	T _j =125 °C, Inductive load	-	-	0.3	-	mJ
r _g	Internal gate resistance	Per switch		-	0	-	Ω

THERMAL RESISTANCE CHARACTERISTICS (per 1/2 module)

Symbol	Itom	Conditions	Limits			Unit
	ltem	Conditions	Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance	Junction to case (Note4)	-	-	0.17	K/W
$R_{th(j-c)D}$		Junction to case (Note4)	-	-	0.49	r\/vv
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, Thermal grease applied (Note4, 6)	-	0.07	-	K/W

Caution; No short-circuit capability is designed.

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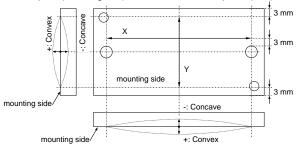
MECHANICAL CHARACTERISTICS

Symbol	Itam	Conditions Min.		Limits		Unit	
	Item			Min.	Тур.	Max.	Unit
M _t	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N⋅m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N⋅m
٨	Creepage distance	Terminal to terminal		17.0	-	-	- mm
ds		Terminal to base plate		28.5	-	-	
da	Clearance	Terminal to terminal		11.0	-	-	mm
	Clearance	Terminal to base plate		25.6	-	-	mm
m	mass	-		-	310	-	g
ec	Flatness of bose plate	On the centerline X (Note7)		-100	-	100	um
	Flatness of base plate	On the centerline Y (Note7)		-100	-	100	μm

^{*:} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (DIODE).

- 2. Junction temperature (T_j) should not increase beyond T_{jmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_j) dose not exceed $T_{j\,m\,a\,x}$ rating.
- 4. Case temperature (T_C) and heat sink temperature (T_s) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise.
- 6. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K).
- 7. The base plate (mounting side) flatness measurement points (X, Y) are as follows of the following figure.

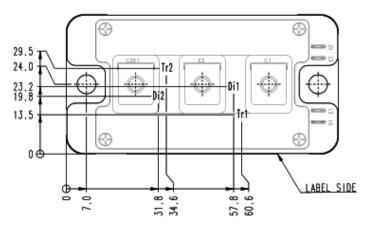


RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Item Conditions		Unit		
	item	Conditions	Min.	Тур.	Max. 800	Offic
Vcc	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	800	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R _G	External gate resistance	Per switch	3.1	-	31	Ω

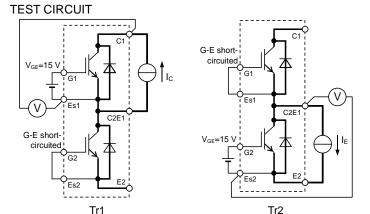
CHIP LOCATION (Top view)

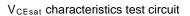
Dimension in mm, tolerance: ±1 mm

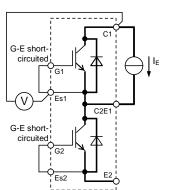


Tr1/Tr2: IGBT, Di1/Di2: DIODE

IGBT Turn-on switching energy IGBT Turn-off switching energy DIODE Reverse recovery energy Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)







Di1

0 V

V_{EC} characteristics test circuit

G-E short

G-E short-

circuited

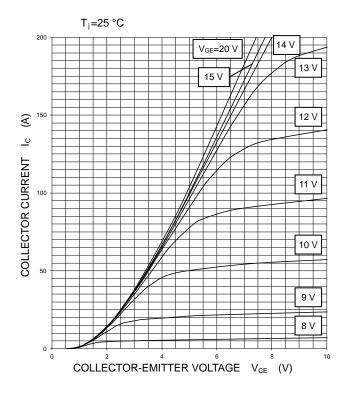
Di2

circuited

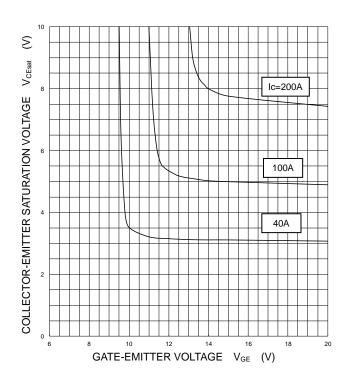
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

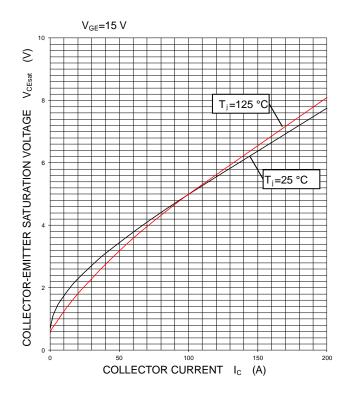
OUTPUT CHARACTERISTICS (TYPICAL)



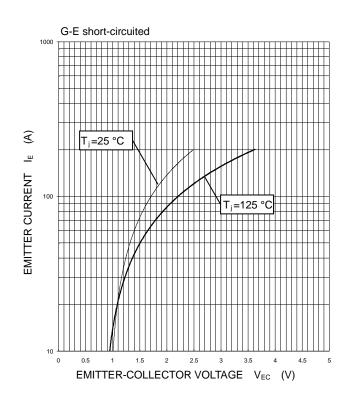
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)

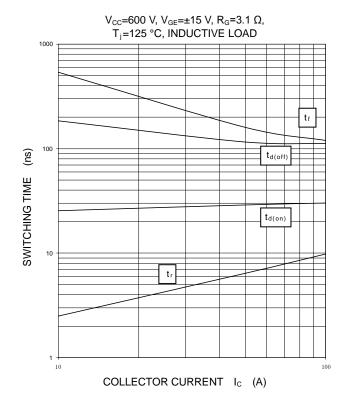


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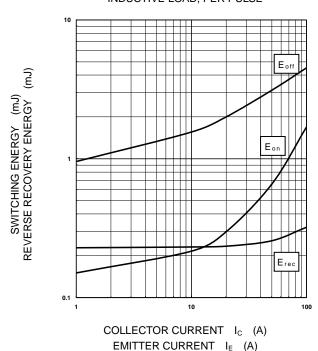
PERFORMANCE CURVES

HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

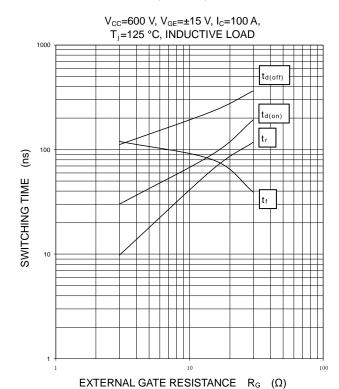


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 V_{CC} =600 V, V_{GE} =±15 V, R_G =3.1 Ω , T_j =125 °C, INDUCTIVE LOAD, PER PULSE

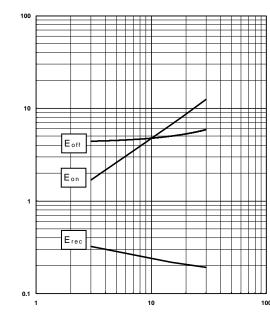


HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

 $V_{\text{CC}}\!\!=\!\!600$ V, $V_{\text{GE}}\!\!=\!\!\pm15$ V, $I_{\text{C}}/I_{\text{E}}\!\!=\!\!100$ A, $T_{j}\!=\!125$ °C, INDUCTIVE LOAD, PER PULSE



EXTERNAL GATE RESISTANCE R_G (Ω)

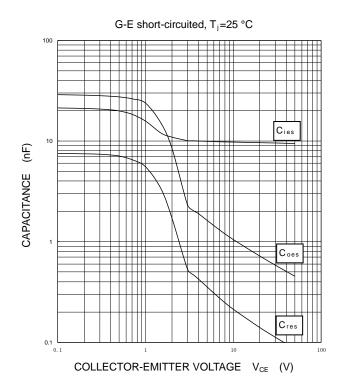
(E

SWITCHING ENERGY (mJ) REVERSE RECOVERY ENERGY

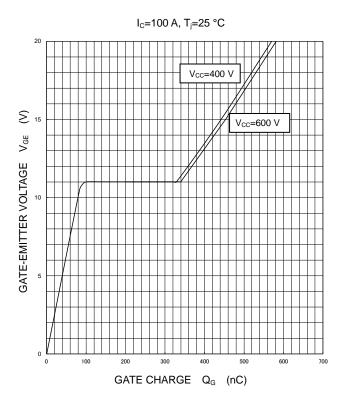
HIGH POWER SWITCHING USE INSULATED TYPE

PERFORMANCE CURVES

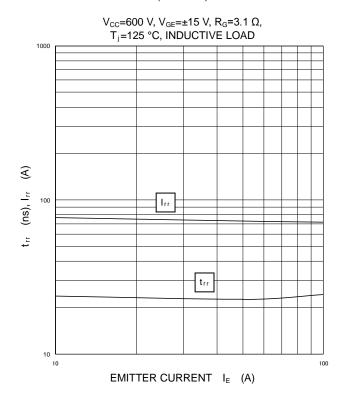
CAPACITANCE CHARACTERISTICS (TYPICAL)



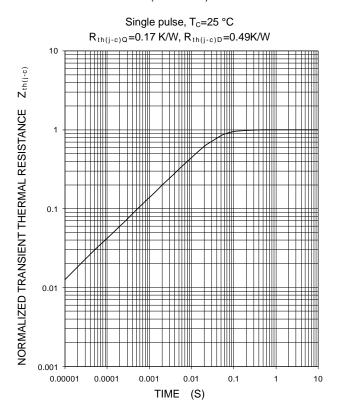
GATE CHARGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTIC S (MAXIMUM)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

Keep safety first in your circuit designs!

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