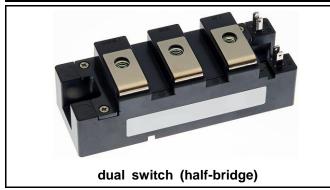


<IGBT Modules>

# **CM150DY-13T**

**HIGH POWER SWITCHING USE INSULATED TYPE** 



Collector current Ic ..... 150A Collector-emitter voltage VCES ..... 6 5 0 V Maximum junction temperature T<sub>vimax</sub> ...... 175°C dual switch (half-bridge) •Copper base plate (Nickel-plating) Nickel-plating tab terminals • RoHS Directive compliant

•UL Recognized under UL1557, File No. E323585

#### APPLICATION

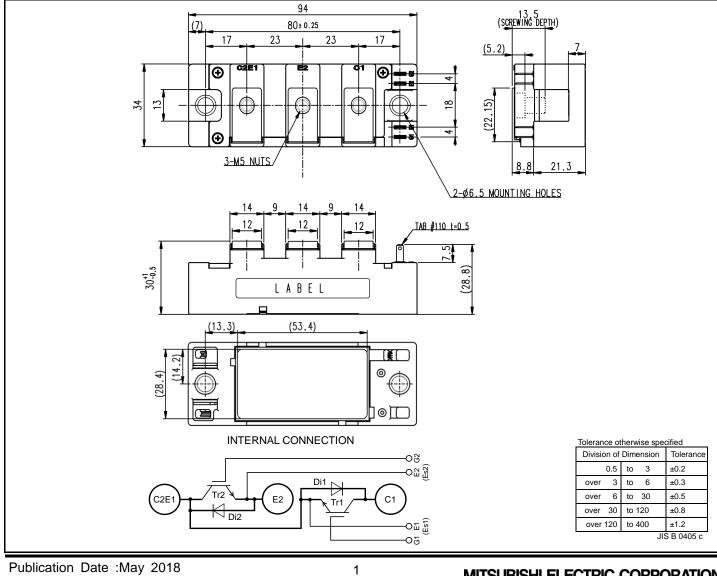
AC Motor Control, Motion/Servo Control, Power supply, etc.

#### **OPTION** (Below options are available.)

•PC-TIM (Phase Change Thermal Interface Material) pre-apply

#### **OUTLINE DRAWING & INTERNAL CONNECTION**

**Dimension in mm** 



## MITSUBISHI ELECTRIC CORPORATION

#### MAXIMUM RATINGS (T<sub>vj</sub>=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	650	V
$V_{\text{GES}}$	Gate-emitter voltage	C-E short-circuited	± 20	V
lc		DC, T <sub>C</sub> =141 °C* (Note2, 4)	150	•
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	300	A
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	1090	W
IE (Note1)		DC (Note2)	150	•
IERM (Note1)	Emitter current	Pulse, Repetitive (Note3)	300	A
Visol	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
$T_{vjmax}$	Maximum junction temperature	Instantaneous event (overload)	175	- °C
T <sub>Cmax</sub>	Maximum case temperature (Note4)		150*	
Tvjop	Operating junction temperature	Continuous operation (under switching)	-40 ~ +150	**
T <sub>stg</sub>	Storage temperature	-	-40 ~ +150*	°C

#### ELECTRICAL CHARACTERISTICS (Tvj=25 °C, unless otherwise specified)

Symbol	ltom	ltom Conditions		Limits			Linit
Symbol	Item Conditions		Min.	Тур.	Max.	Unit	
ICES	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μA
V <sub>GE(th)</sub>	Gate-emitter threshold voltage	I <sub>C</sub> =15 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =150 A, V <sub>GE</sub> =15 V,	T <sub>vj</sub> =25 °C	-	1.40	1.70	v
V <sub>CEsat</sub>		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.50	-	
(Terminal)		(Note5)	T <sub>vj</sub> =150 °C	-	1.55	-	
	Collector-emitter saturation voltage	I <sub>C</sub> =150 A,	T <sub>vj</sub> =25 °C	-	1.30	1.55	v
V <sub>CEsat</sub>		V <sub>GE</sub> =15 V,	T <sub>vj</sub> =125 °C	-	1.35	-	
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.35	-	
Cies	Input capacitance				-	20.1	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	0.9	nF
Cres	Reverse transfer capacitance	7	-	-	0.4		
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =300 V, I <sub>C</sub> =150 A, V <sub>GE</sub> =15 V		-	0.62	-	μC
t <sub>d(on)</sub>	Turn-on delay time	V <sub>CC</sub> =300 V, I <sub>C</sub> =150 A, V <sub>GE</sub> =±15 V,		-	-	300	ns
t <sub>r</sub>	Rise time			-	-	150	
t <sub>d(off)</sub>	Turn-off delay time			-	-	400	
t <sub>f</sub>	Fall time	$-$ R <sub>G</sub> =1.0 $\Omega$ , Inductive load		-	-	400	
a	<ul> <li>Emitter-collector voltage</li> </ul>	I <sub>E</sub> =150 A, G-E short-circuited,	T <sub>vj</sub> =25 °C	-	2.10	2.90	v
V <sub>EC</sub> (Note.1)		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	2.05	-	
(Terminal)		(Note5)	T <sub>vj</sub> =150 °C	-	2.05	-	
(Nata 4)		I <sub>E</sub> =150 A,	T <sub>vj</sub> =25 °C	-	1.90	2.65	5
V <sub>EC</sub> (Note.1)		G-E short-circuited,	T <sub>vj</sub> =125 °C	-	1.80	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.80	-	
t <sub>rr</sub> <sup>(Note1)</sup>	Reverse recovery time	V <sub>CC</sub> =300 V, I <sub>E</sub> =150 A, V <sub>GE</sub> =±15 V,		-	-	150	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge	$R_{G}$ =1.0 $\Omega$ , Inductive load		-	5.0	-	μC
Eon	Turn-on switching energy per pulse	$V_{CC}$ =300 V, $I_{C}$ = $I_{E}$ =150 A,		-	2.1	-	
E <sub>off</sub>	Turn-off switching energy per pulse	V <sub>GE</sub> =±15 V, R <sub>G</sub> =1.0 Ω, T <sub>vj</sub> =150 °C,		-	7.1	-	mJ
Err (Note1)	Reverse recovery energy per pulse	Inductive load		-	2.7	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =25 °C (Note4)		-	0.2	-	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	4.0	-	Ω

\*: The value of PC-TIM applied module is limited by the heat resistant temperature of PC-TIM.

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
		Conditions	Min.	Тур.	Max.	Unit
$R_{th(j-c)Q}$		Junction to case, per Inverter IGBT (Note4)	-	-	137	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)	-	-	235	r./kvv
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, per 1 module Thermal grease applied (Note4, 6)	-	36.6	-	K/kW

#### **MECHANICAL CHARACTERISTICS**

Symbol	ltom	Conditions		Limits			Unit
	Item			Min.	Тур.	Max.	Unit
Mt	Mounting torque	Main terminals	M 5 screw	2.5	3.0	3.5	N∙m
Ms	Mounting torque	Mounting to heat sink	M 6 screw	3.5	4.0	4.5	N∙m
ds	Creepage distance	Terminal to terminal		18.4	-	-	mm
		Terminal to base plate		21.1	-	-	
da	Clearance	Terminal to terminal		9.6	-	-	
	Clearance	Terminal to base plate		16.7	-	-	mm
ec	Flatness of base plate	On the centerline (Note7)		±0	-	+200	μm
m	mass	-		-	120	-	g

\*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU. Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

2. Junction temperature  $(T_{vj})$  should not increase beyond  $T_{vjmax}$  rating.

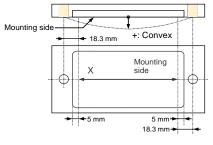
3. Pulse width and repetition rate should be such that the device junction temperature  $(T_{vj})$  dose not exceed  $T_{vjmax}$  rating.

4. Case temperature (T<sub>c</sub>) and heat sink temperature (T<sub>s</sub>) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.

5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6. Typical value is measured by using thermally conductive grease of  $\lambda$ =3.0 W/(m·K)/D<sub>(C-S)</sub>=50 µm.

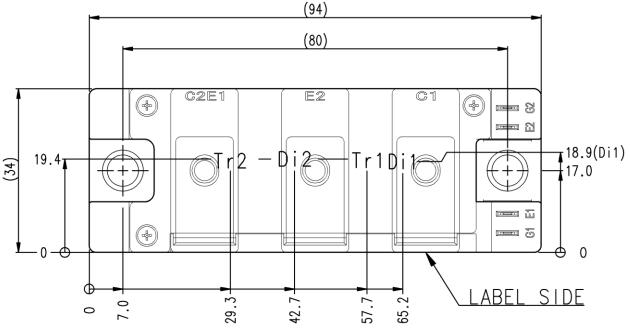
7. The base plate (mounting side) flatness measurement point is as follows of the following figure.



#### **RECOMMENDED OPERATING CONDITIONS**

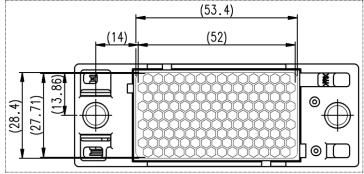
Symbol	ltom	Conditions	Limits			Unit
	Item	Conditions	Min.	Тур.	Max.	Unit
V <sub>cc</sub>	(DC) Supply voltage	Applied across C1-E2 terminals	-	300	450	V
$V_{\text{GEon}}$	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R <sub>G</sub>	External gate resistance	Per switch	1.0	-	47	Ω

#### CHIP LOCATION (Top view)



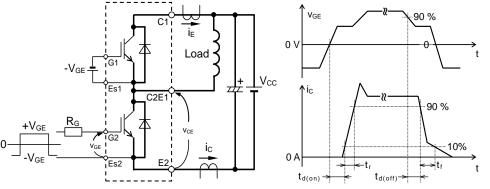
#### Tr1/Tr2: IGBT, Di1/Di2: FWD

#### **Option: PC-TIM applied baseplate outline**



Dimension in mm, tolerance: ±1 mm

#### TEST CIRCUIT AND WAVEFORMS



Switching characteristics test circuit and waveforms

trr, Qrr characteristics test waveform

Q<sub>rr</sub>=0.5×I<sub>rr</sub>×t<sub>rr</sub>

0.5×1<sub>rr</sub>

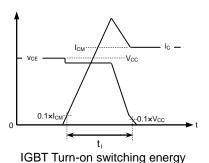
trr

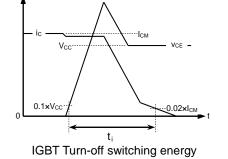
İE

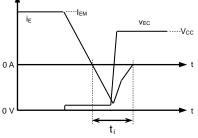
0 A

ΙE

 $I_{rr}$ 



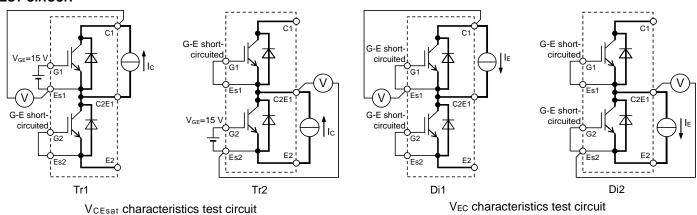




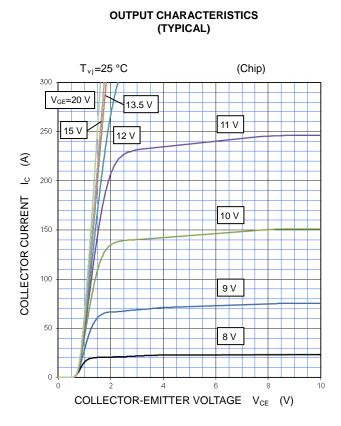
FWD Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

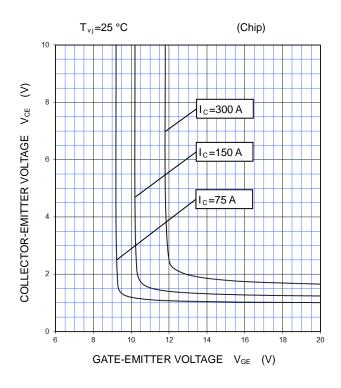
#### **TEST CIRCUIT**

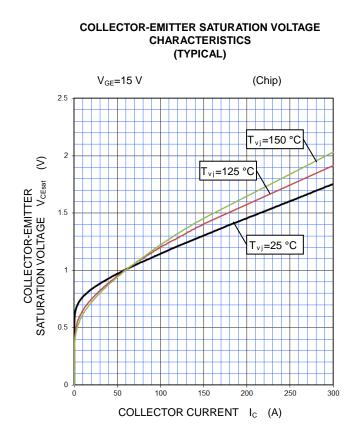


#### PERFORMANCE CURVES

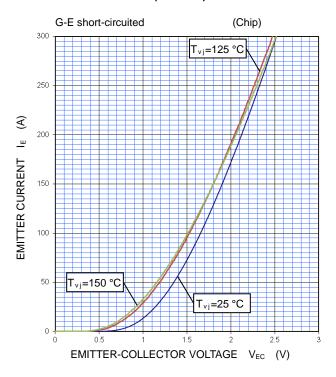


## COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)

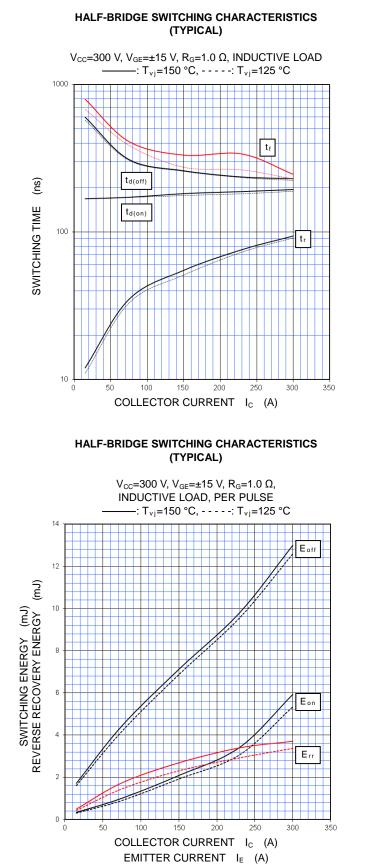


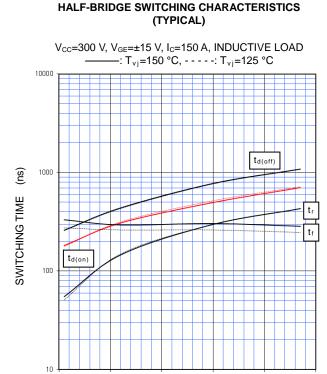


#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



#### PERFORMANCE CURVES





#### HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

EXTERNAL GATE RESISTANCE  $R_{G}$  ( $\Omega$ )

30

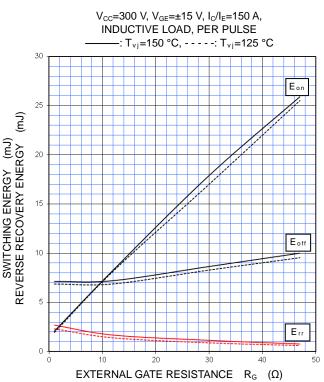
40

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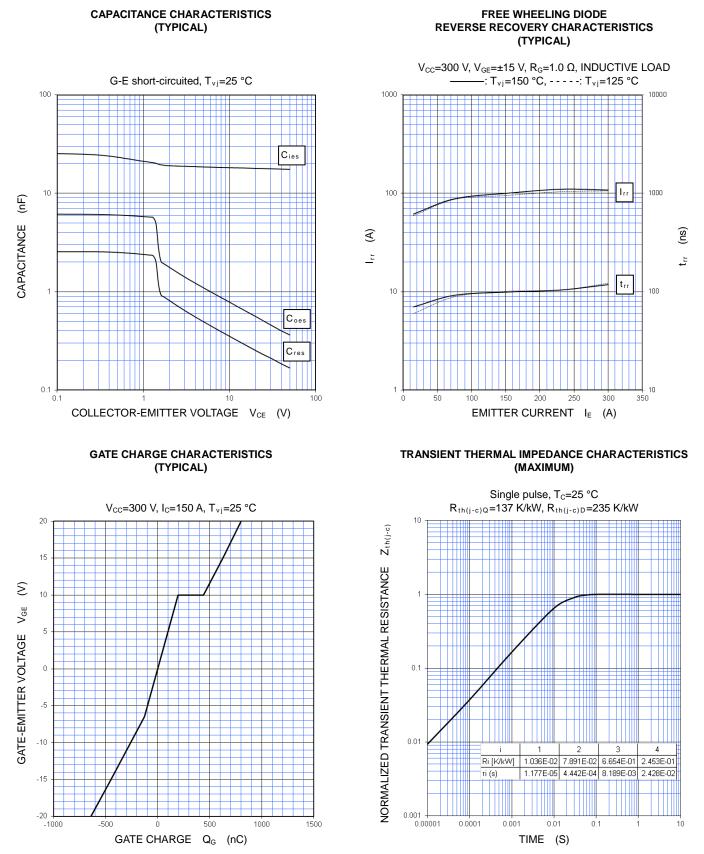
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ò



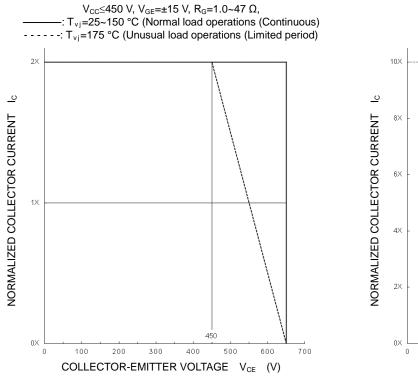
#### PERFORMANCE CURVES



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

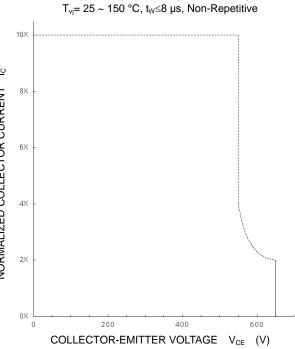
#### PERFORMANCE CURVES

#### TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)





 $V_{CC} \le 400 \text{ V}, V_{GE} = \pm 15 \text{ V}, R_G = 1.0 - 47 \Omega,$ 



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