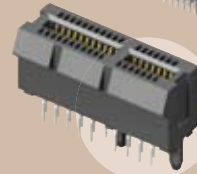


PCIE-064-02-F-D-RA



PCIE-036-02-F-D-TH

PCI EXPRESS® CARD SOCKETS

SPECIFICATIONS

For complete specifications and recommended PCB layouts see www.samtec.com?PCIE

Insulator Material:

Black Nylon

Contact:

Phosphor Bronze

Plating:

Au or Sn over

50µ" (1,27µm) Ni

Current Rating:

2.5A @ 80°C ambient

Operating Temp:

-55°C to +125°C

Card Insertion Depth:

(8,00mm) .315" nominal

RoHS Compliant:

Yes

Lead-Free Solderable:

Wave only



Mates with:
(1,60mm) .062" card



Supports one, four, eight and sixteen PCI Express® links

Choice of card-edge mount or PCB mount

(1,00mm) .03937" pitch

Accepts 1,6mm card

Alignment pins

APPLICATION SPECIFIC OPTION

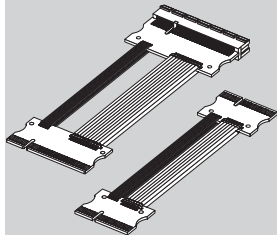
Other plating options available. Call Samtec.

PCIE	Rated @ 3dB Insertion Loss
11,1mm Stack Height	
Differential Pair Signaling	7.0 GHz / 14 Gbps

Performance data for other stack heights and complete test data available at www.samtec.com?PCIE or contact sig@samtec.com

ALSO AVAILABLE

PCI Express® Jumpers & Extenders call for PCIEC Series



- Loop back Extender (From one PCIe® slot to another PCIe® slot)
 - Ser-Des Physical Extender (From one PCIe® slot to another PCIe® Ser-Des)
 - Physical Extender for easy troubleshooting of PCIe® card debug and analysis (PCIe® slot to Emulator or Analyzer)
- Call Samtec.

PCI Express® is a registered trademark of PCI-SIG

Note: Some lengths, styles and options are non-standard, non-returnable.

PCIE - NO. OF POSITIONS - 02 - PLATING OPTION - D - TAIL OPTION

-036, -064, -098, -164

-F

= Gold flash on contact, Tin on tail

-EMS2

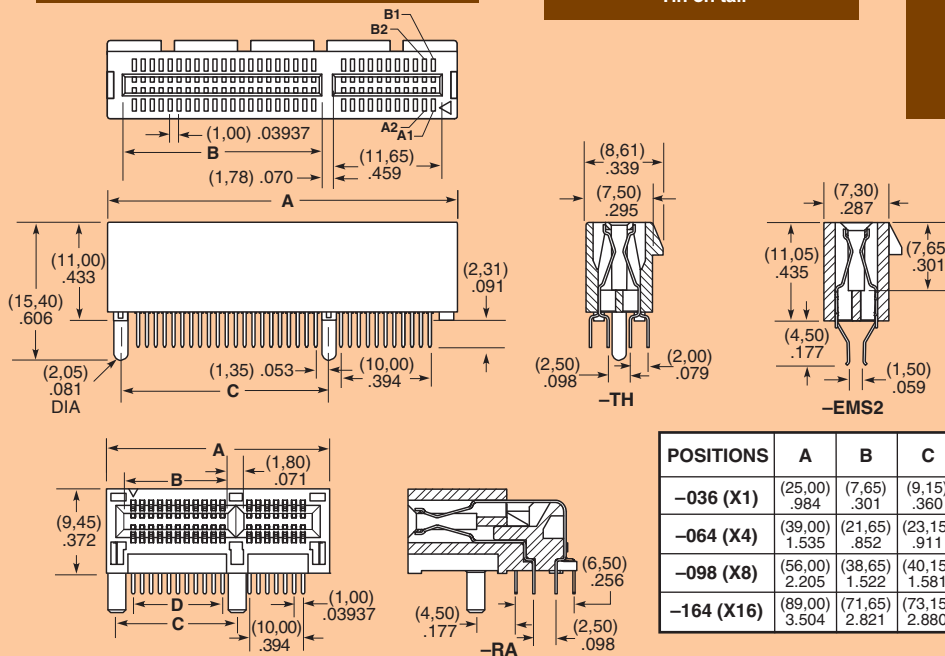
= Edge Mount

-TH

= Through-hole

-RA

= Right Angle



POSITIONS	A	B	C	D
-036 (X1)	(25,00) .984	(7,65) .301	(9,15) .360	(6,00) .236
-064 (X4)	(39,00) 1.535	(21,65) .852	(23,15) .911	(20,00) .787
-098 (X8)	(56,00) 2.205	(38,65) 1.522	(40,15) 1.581	(37,00) 1.457
-164 (X16)	(89,00) 3.504	(71,65) 2.821	(73,15) 2.880	(70,00) 2.756

Due to technical progress, all designs, specifications and components are subject to change without notice.